

Pin Report (Sheet CONNECTOR/LED/IOEXP) for OZY_REVXA8.sch on 8/18/2006 4:31:02 PM

Name	Signal	Number	Function Name
J8.1	FPGA_GPIO13_SEL	1	
J8.2	BF_GPIO14_SEL	2	
J8.3	BF_GPIO15_SEL	3	
J8.4	BF_GPIO16_SEL	4	
J8.5	GND	5	
J8.6	BF_GPIO22	6	
J8.7	BF_GPIO23	7	
J8.8	BF_GPIO24	8	
J8.9	VCOM_SEL	9	
J20.1	BF_GPIO9	1	STRB(O)
J20.2	BF_GPIO1	2	D0
J20.3	BF_GPIO2	3	D1
J20.4	BF_GPIO3	4	D2
J20.5	BF_GPIO4	5	D3
J20.6	BF_GPIO5	6	D4
J20.7	BF_GPIO6	7	D5
J20.8	BF_GPIO7	8	D6
J20.9	BF_GPIO8	9	D7
J20.10	BF_GPIO17	10	ACK(I)
J20.11	BF_GPIO18	11	BUSY(I)
J20.12	BF_GPIO19	12	PO(I)
J20.13	BF_GPIO20	13	SEL(I)
J20.14	BF_GPIO10	14	AF(O)
J20.15	BF_GPIO21	15	ERR(I)
J20.16	BF_GPIO11	16	RST(O)
J20.17	BF_GPIO12	17	SEL(O)
J20.18	GND	18	GND
J20.19	GND	19	GND
J20.20	GND	20	GND
J20.21	GND	21	GND
J20.22	GND	22	GND
J20.23	GND	23	GND
J20.24	GND	24	GND
J20.25	GND	25	GND