

ATLAS BUS SIGNALS								
DIN41612 96 PIN								
XBUS			YBUS					
PIN	NAME	ALTERNATE	PIN	NAME	PIN	NAME	ALTERNATE	
A1	+12VDC		B1	+12VDC	C1	+12VDC		
A2	A2		B2	GND	C2	C2		
A3	A3		B3	GND	C3	C3		
A4	A4		B4	GND	C4	C4		
A5	A5		B5	GND	C5	C5		
A6	A6		B6	GND	C6	C6		
A7	A7		B7	GND	C7	C7		
A8	A8		B8	GND	C8	C8		
A9	A9		B9	GND	C9	C9		
A10	A10		B10	GND	C10	C10		
A11	A11		B11	GND	C11	C11		
A12	A12		B12	GND	C12	C12		
A13	A13		B13	GND	C13	C13		
A14	A14		B14	GND	C14	C14		
A15	A15		B15	GND	C15	C15		
A16	A16		B16	GND	C16	C16		
A17	A17		B17	GND	C17	C17		
A18	A18	SPI - nCS4	B18	GND	C18	C18		1-WIRE
A19	A19	SPI - nCS3	B19	GND	C19	C19		nRST (1)
A20	A20	SPI - nCS2	B20	GND	C20	C20		I2C - SCL
A21	A21	SPI - nCS1	B21	GND	C21	C21		I2C - SDA
A22	A22	SPI - nCS0	B22	GND	C22	C22		JTAG - TRST
A23	A23	SPI - SCK	B23	GND	C23	C23		JTAG - TMS
A24	A24	SPI - MISO	B24	GND	C24	C24		JTAG - TCK
A25	A25	SPI - MOSI	B25	GND	C25	C25		JTAG - SDO ret
A26	-12VDC		B26	-12VDC	C26	-12VDC		
A27	A27	JTAG - SDO	B27	GND	C27	C27		SPI - MOSI ovfl out
A28	-5VDC		B28	-5VDC	C28	-5VDC		
A29	A29	JTAG - SDI	B29	GND	C29	C29		SPI - MOSI ovfl in
A30	+3.3VDC		B30	+3.3VDC	C30	+3.3VDC		
A31	A31		B31	GND	C31	C31		
A32	+5VDC		B32	+5VDC	C32	+5VDC		

Notes:

- 1 Reset line, Open collector pulled up to +3.3V

ATLAS OZY-JANUS BUS SIGNALS								
DIN41612 96 PIN								
XBUS			YBUS					
PIN	NAME	OZY-JANUS	PIN	NAME	PIN	NAME	OZY-JANUS	
A1	+12VDC		B1	+12VDC	C1	+12VDC		
A2	A2		B2	GND	C2	C2		DFS0
A3	A3		B3	GND	C3	C3		DFS1
A4	A4		B4	GND	C4	C4		PTT
A5	A5		B5	GND	C5	C5		

A6	A6		B6	GND	C6	C6	<i>CLK_24MHz</i>
A7	A7		B7	GND	C7	C7	
A8	A8		B8	GND	C8	C8	<i>FSYNC</i>
A9	A9		B9	GND	C9	C9	<i>CBCLK</i>
A10	A10		B10	GND	C10	C10	<i>CDIN</i>
A11	A11		B11	GND	C11	C11	<i>CDOUT</i>
A12	A12		B12	GND	C12	C12	<i>CLRCIN/CLRCOUT</i>
A13	A13		B13	GND	C13	C13	<i>IPWM</i>
A14	A14		B14	GND	C14	C14	<i>QPWM</i>
A15	A15		B15	GND	C15	C15	<i>LRCLK</i>
A16	A16		B16	GND	C16	C16	<i>SDOUT</i>
A17	A17		B17	GND	C17	C17	<i>SCLK</i>
A18	A18	SPI - nCS4	B18	GND	C18	C18	1-WIRE
A19	A19	SPI - nCS3	B19	GND	C19	C19	nRST (1)
A20	A20	SPI - nCS2	B20	GND	C20	C20	I2C - SCL
A21	A21	SPI - nCS1	B21	GND	C21	C21	I2C - SDA
A22	A22	SPI - nCS0	B22	GND	C22	C22	JTAG - TRST
A23	A23	SPI - SCK	B23	GND	C23	C23	JTAG - TMS
A24	A24	SPI - MISO	B24	GND	C24	C24	JTAG - TCK
A25	A25	SPI - MOSI	B25	GND	C25	C25	JTAG - SDO ret
A26	-12VDC		B26	-12VDC	C26	-12VDC	
A27	A27	JTAG - SDO	B27	GND	C27	C27	SPI - MOSI ovfl out
A28	-5VDC		B28	-5VDC	C28	-5VDC	
A29	A29	JTAG - SDI	B29	GND	C29	C29	SPI - MOSI ovfl in
A30	+3.3VDC		B30	+3.3VDC	C30	+3.3VDC	
A31	A31		B31	GND	C31	C31	
A32	+5VDC		B32	+5VDC	C32	+5VDC	