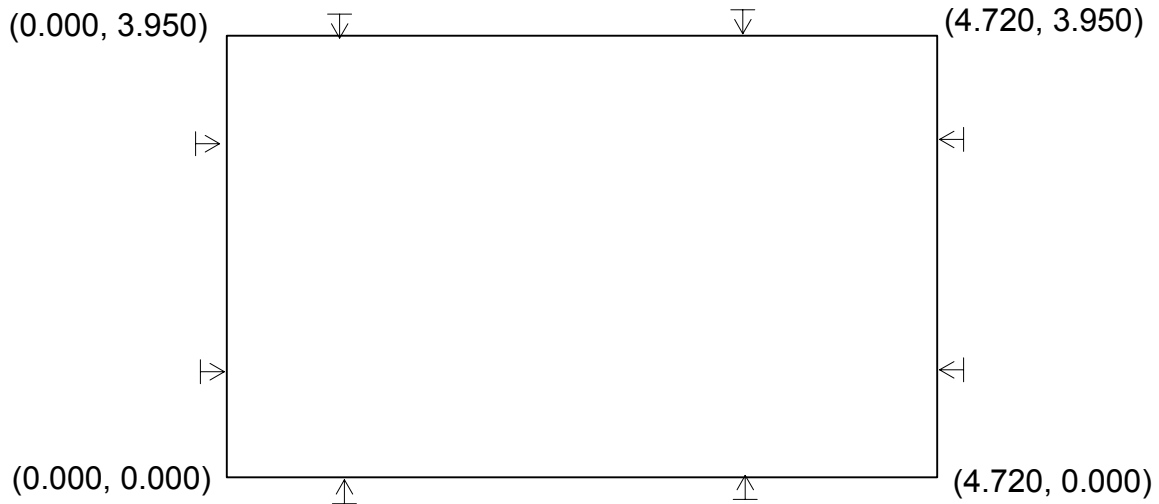



JANUS PCB Rev XC, Cover Sheet
L Johnson, KK7P
2006 December 23 [Saturday]



PCB Material FR4, thickness 0.062" nominal, 1 ounce copper, 4 layers, white silkscreen, green soldermask. There is no gold plating, unless otherwise required.

Stackup order: SST, SMT, TOP, PWR, GND, BOT, SMB

Datum is the lower left corner of the PCB as shown in the drawing above. The provided Gerber and Excellon files share this same datum.

 This symbol represents the approximate break-off tab locations if the boards are panelized.

All holes to be plated through except two (2) 0.126" diameter holes as called out in the Drill Drawing. These are at coordinates (4.310, 3.010) and (4.310, 3.410).

[end]