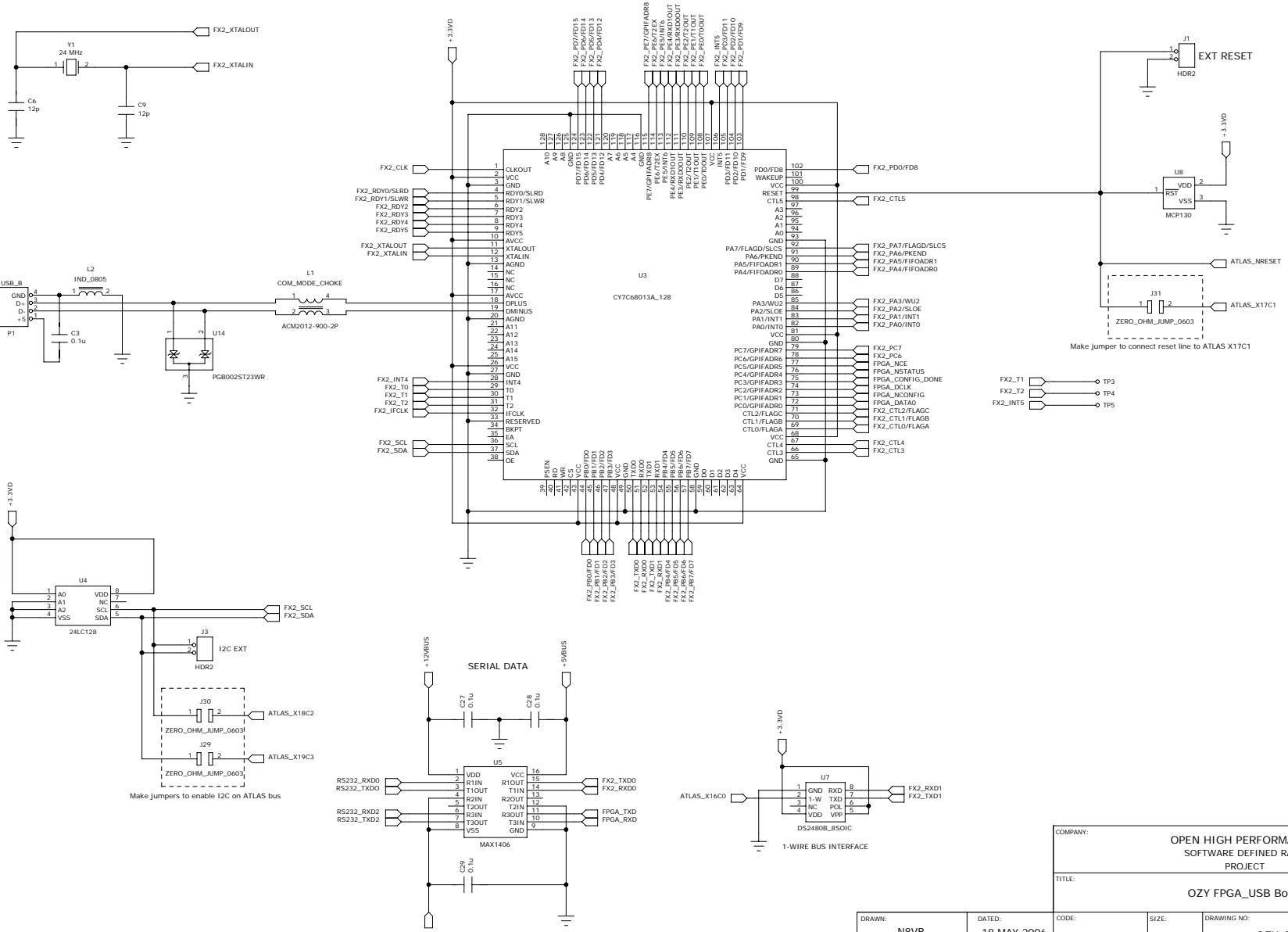


REVISION RECORD		



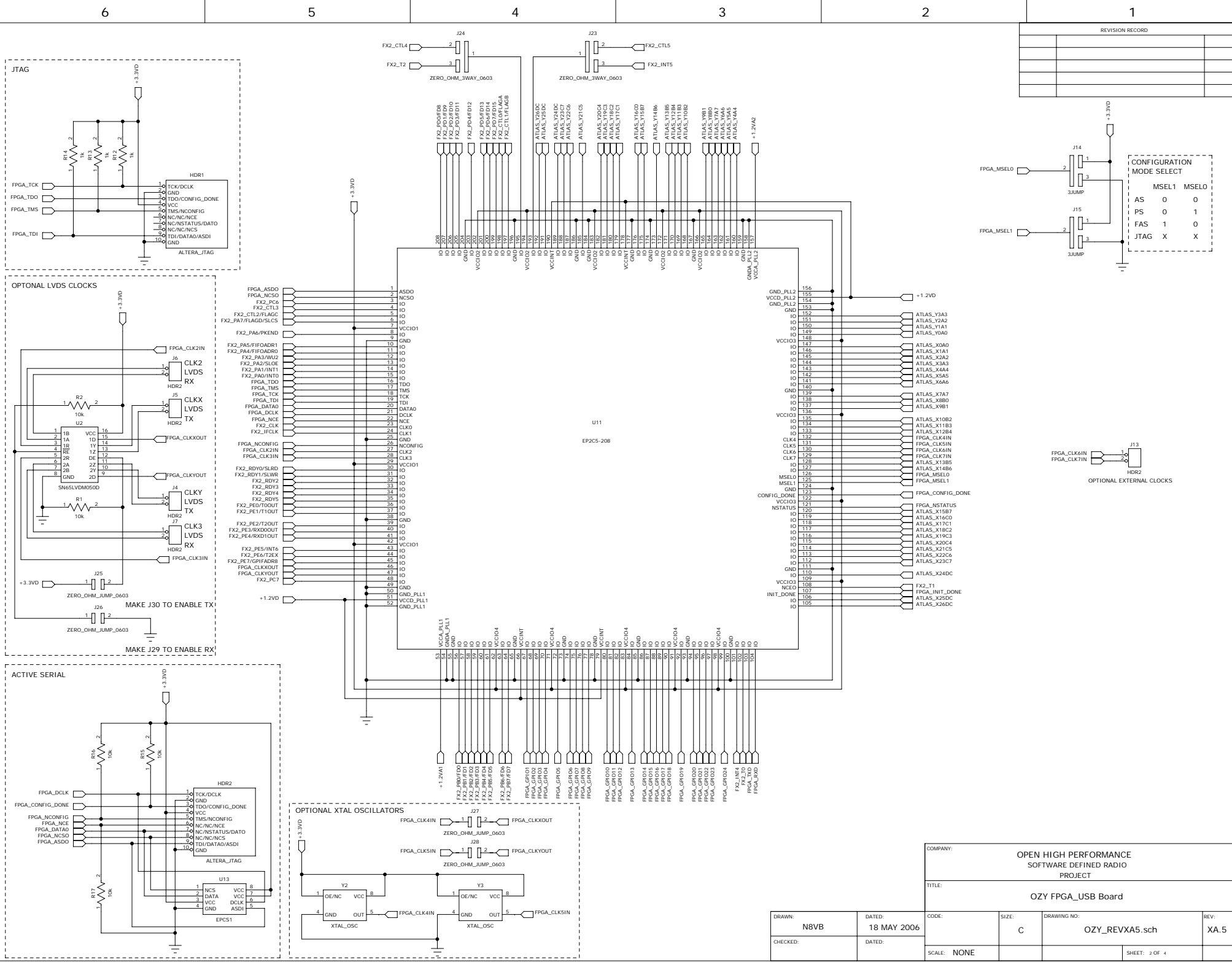
COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
DRAWN:	N8VB	DATED:	18 MAY 2006	CODE:	
CHECKED:		DATED:		SIZE:	C
SCALE:		NONE		DRAWING NO:	OZY_REVXA5.sch
				REV:	XA.5
				SHEET: 1 OF 4	

D

C

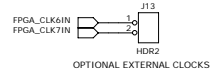
B

A



REVISION RECORD		

CONFIGURATION MODE SELECT		
MSEL1	MSEL0	
AS	0	0
PS	0	1
FAS	1	0
JTAG	X	X



COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
N8VB	18 MAY 2006		C	OZY_REVXA5.sch	XA.5
CHECKED:	DATED:	SCALE: NONE			
				SHEET: 2 OF 4	

D

C

B

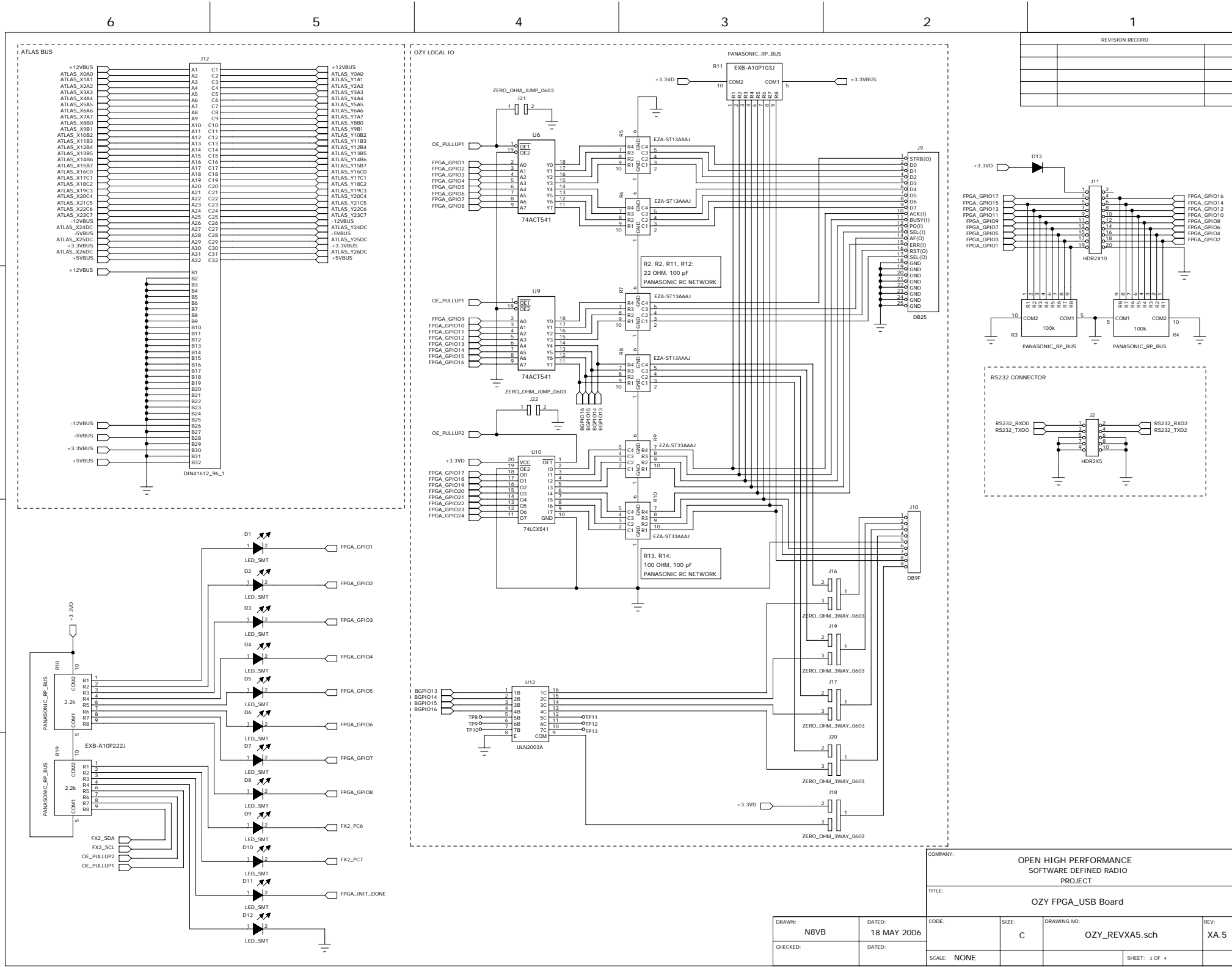
A

D

C

B

A



COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
DRAWN:	N8VB	DATED:	18 MAY 2006	CODE:	
CHECKED:		DATED:		SIZE:	C
		SCALE: NONE		DRAWING NO:	OZY_REVXA5.sch
				SHEET:	3 OF 4
				REV:	XA.5

REVISION RECORD		

POWER SUPPLY

