

D

C

B

A

D

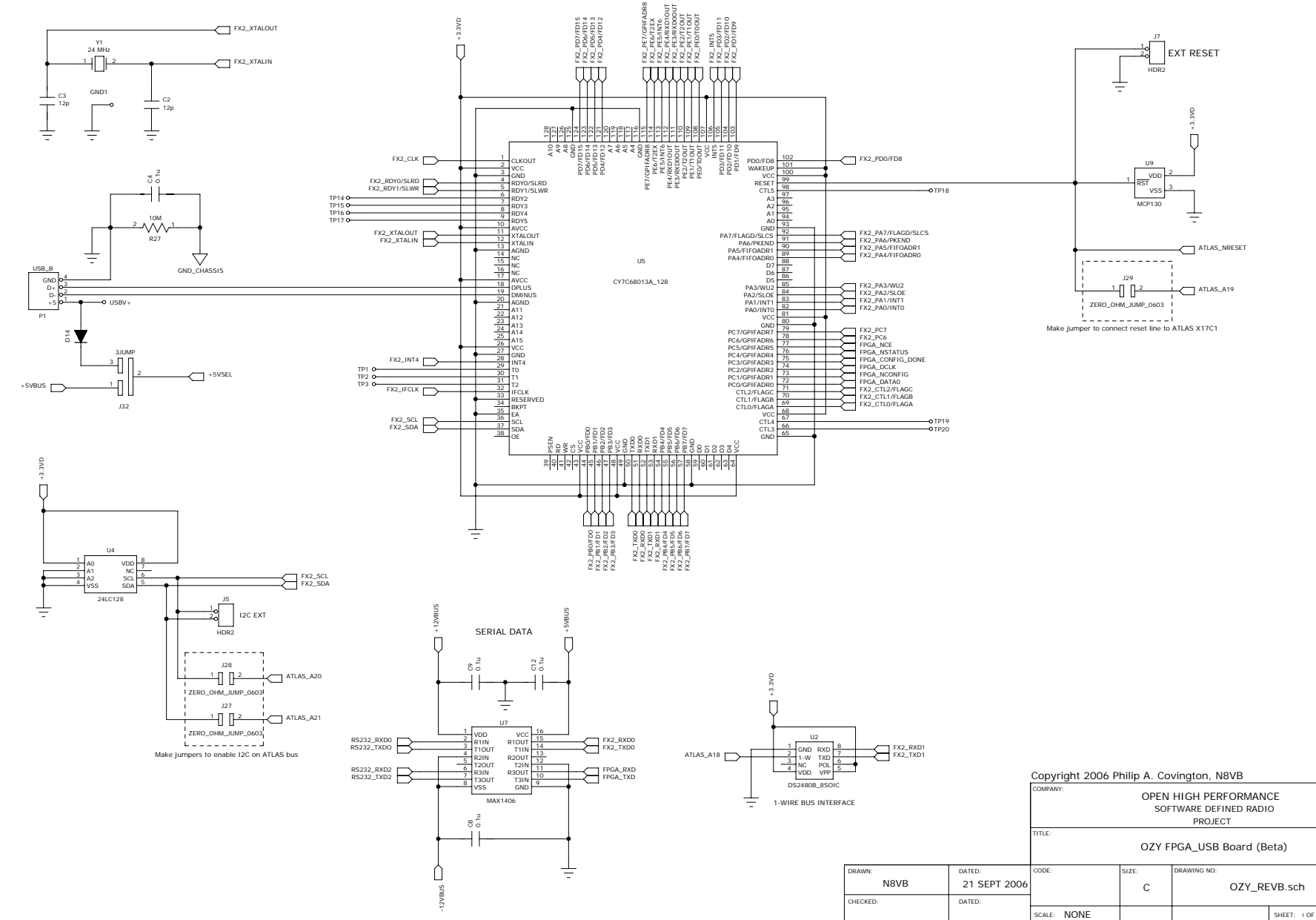
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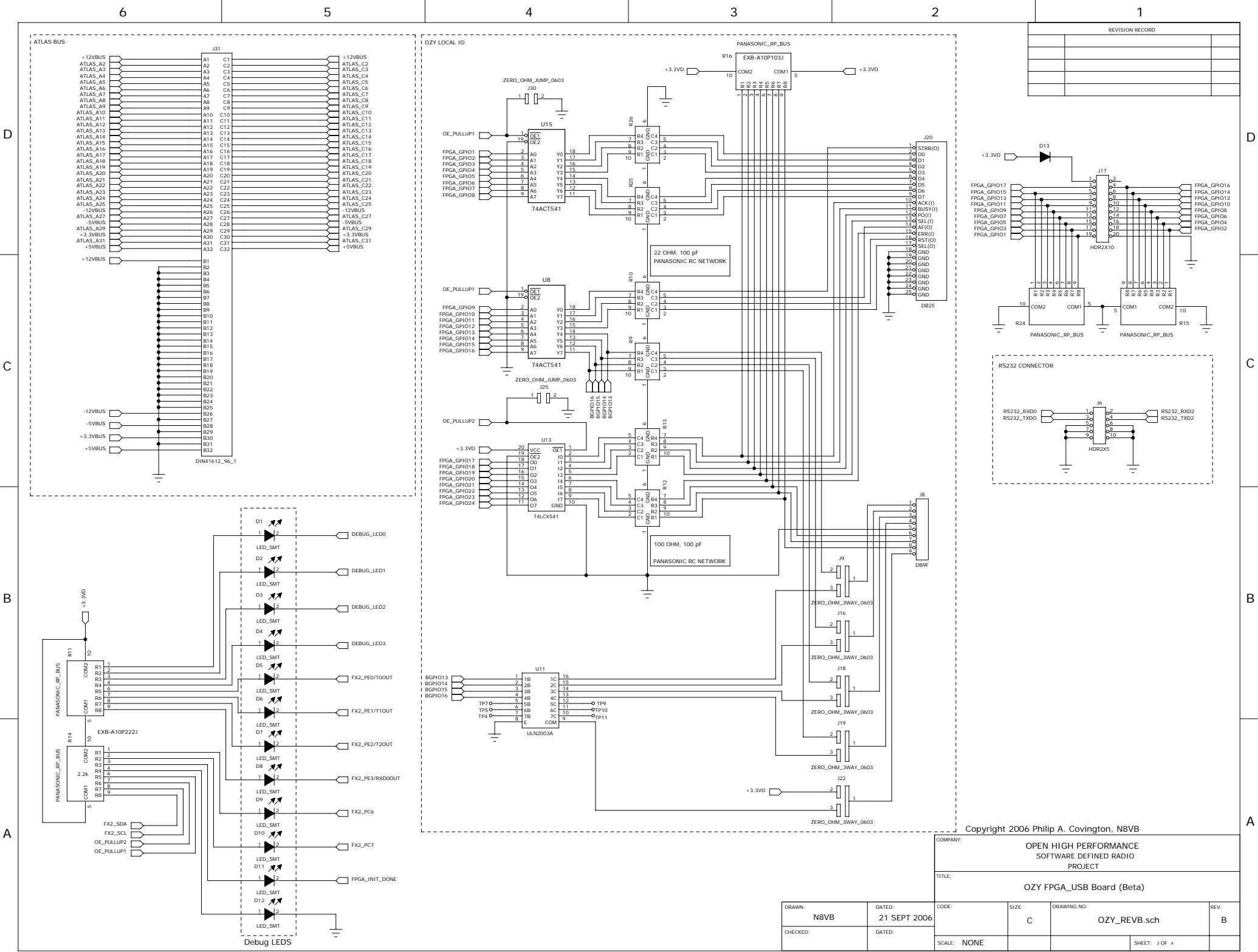
B

A

6 5 4 3 2 1

REVISION RECORD		



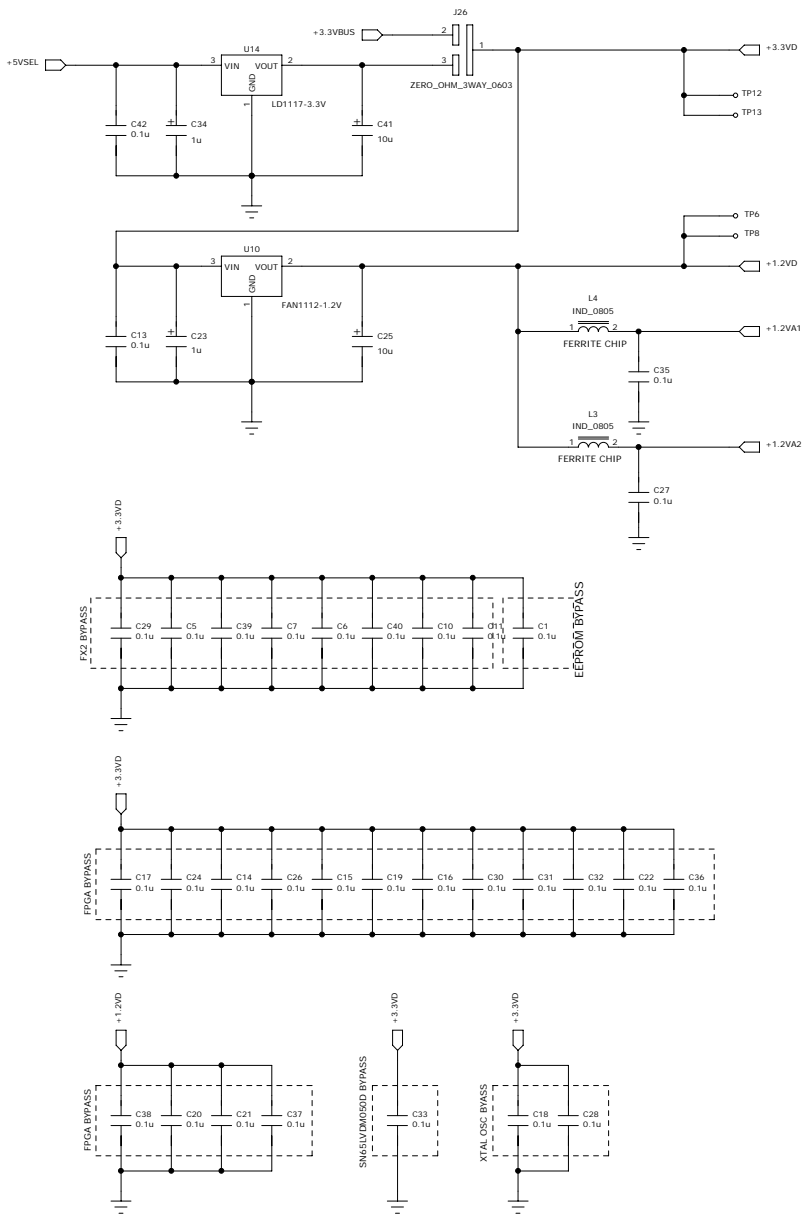


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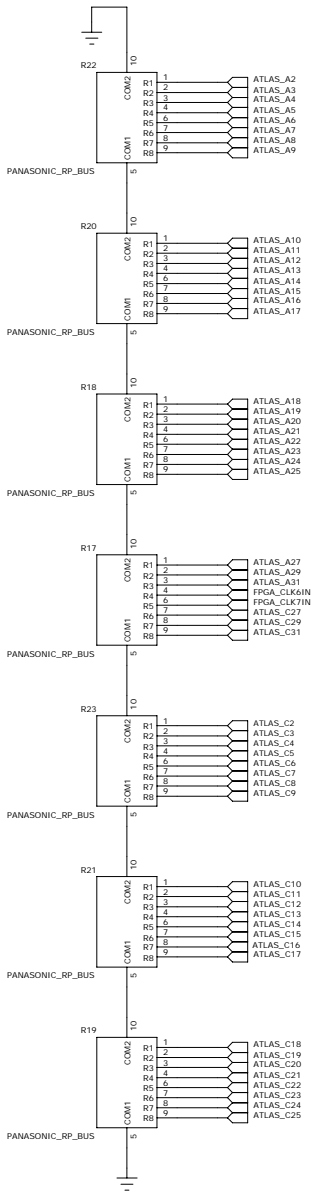
COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board (Beta)			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
N8VB	21 SEPT 2006		C	OZY_REVb.sch	B
CHECKED:	DATED:	SCALE:	NONE	SHEET:	3 OF 4

REVISION RECORD		

POWER SUPPLY



FPGA_IO_TERMINATIONS



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COMPANY: OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT

TITLE: OZY FPGA_USB Board (Beta)

DRAWN: N8VB	DATED: 21 SEPT 2006	CODE:	SIZE: C	DRAWING NO: OZY_REV.B.sch	REV: B
CHECKED:	DATED:	SCALE: NONE		SHEET: 4 OF 4	