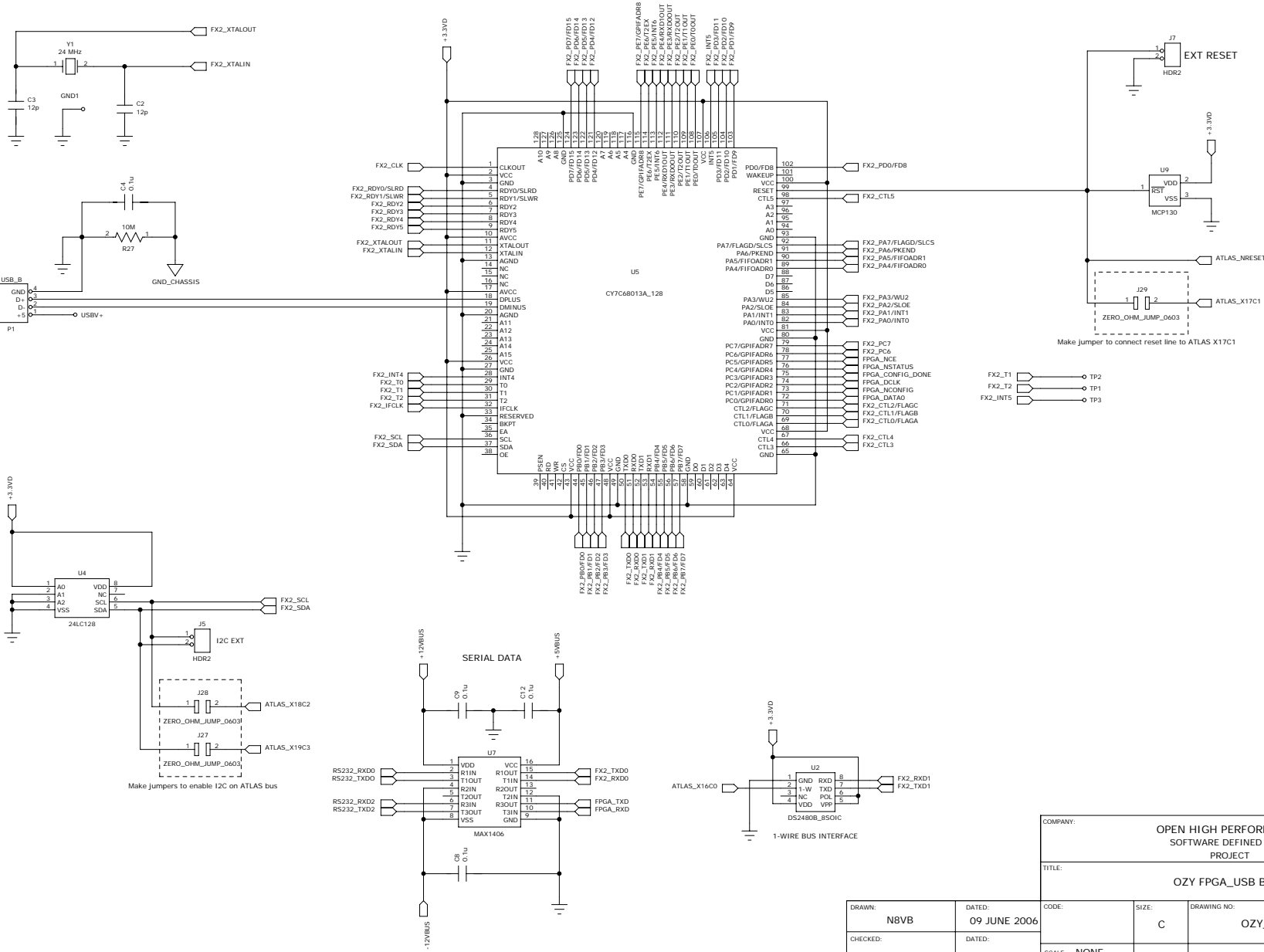


REVISION RECORD		



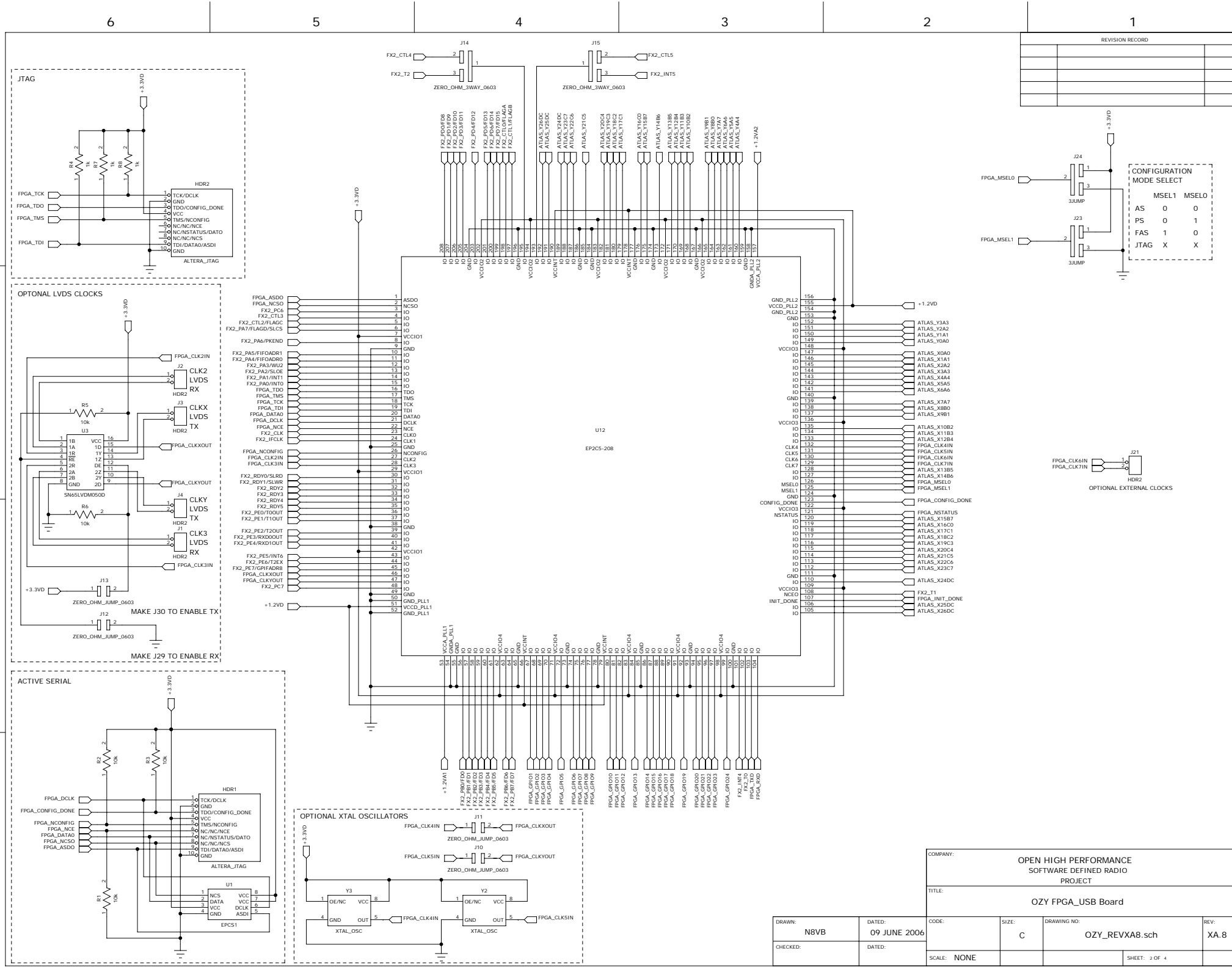
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TITLE:		OZY FPGA_USB Board			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
N8VB	09 JUNE 2006		C	OZY_REVXA8.sch	XA.8
CHECKED:	DATED:	SCALE: NONE			SHEET: 1 OF 4

D

C

B

A



D

C

B

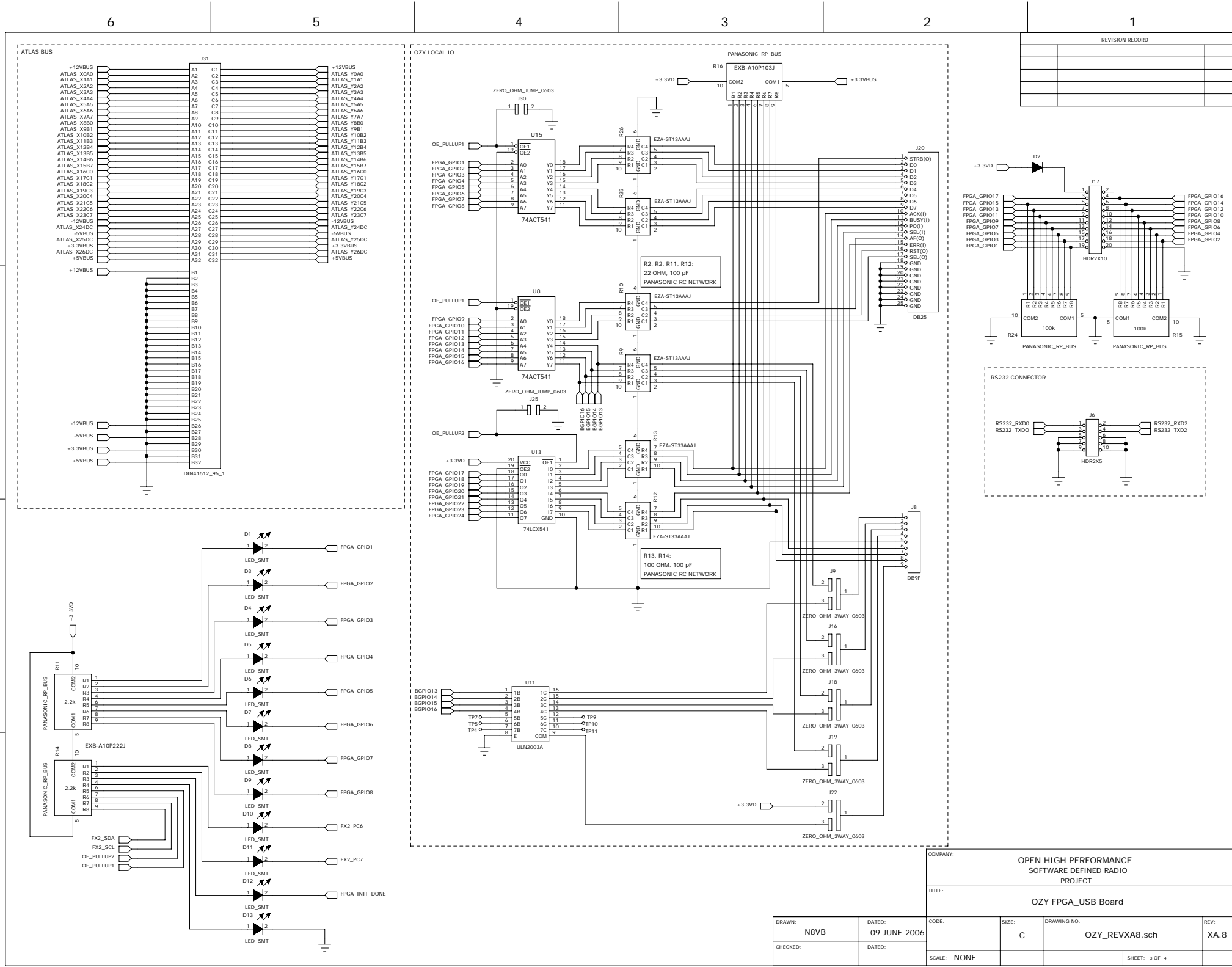
A

D

C

B

A



D

C

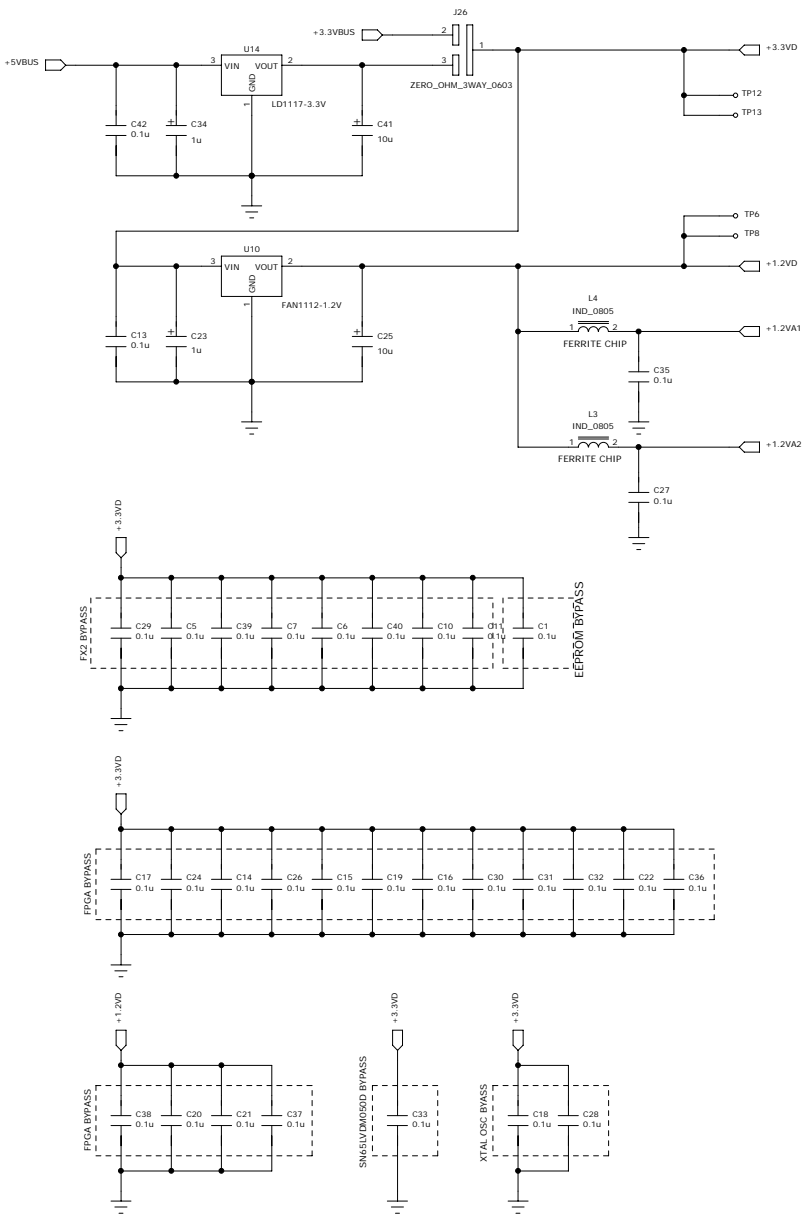
B

A

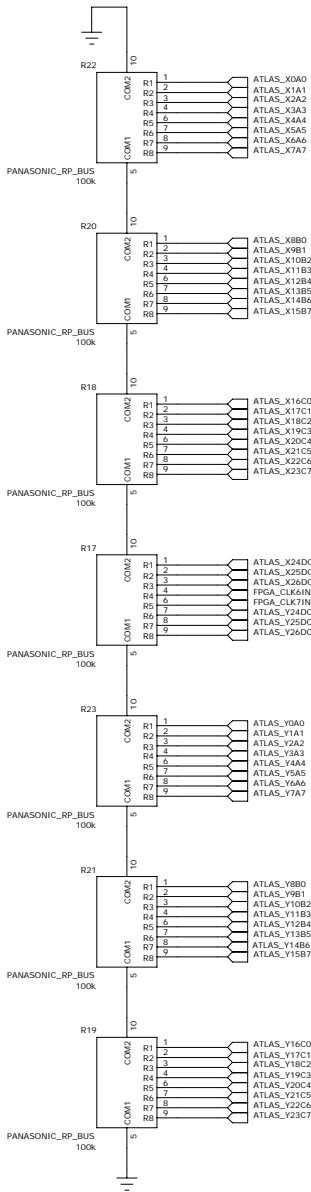
COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
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CHECKED:		DATED:		SIZE:	C
		SCALE: NONE		DRAWING NO:	OZY_REVXA8.sch
				SHEET:	3 OF 4
				REV:	XA.8

REVISION RECORD		

POWER SUPPLY



FPGA_IO_TERMINATIONS



COMPANY: OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT					
TITLE: OZY FPGA_USB Board					
DRAWN: N8VB	DATED: 09 JUNE 2006	CODE:	SIZE: C	DRAWING NO: OZY_REVXA8.sch	REV: XA.8
CHECKED:	DATED:	SCALE: NONE		SHEET: 4 OF 4	