

6

5

4

3

2

1

REVISION RECORD

D

D

C

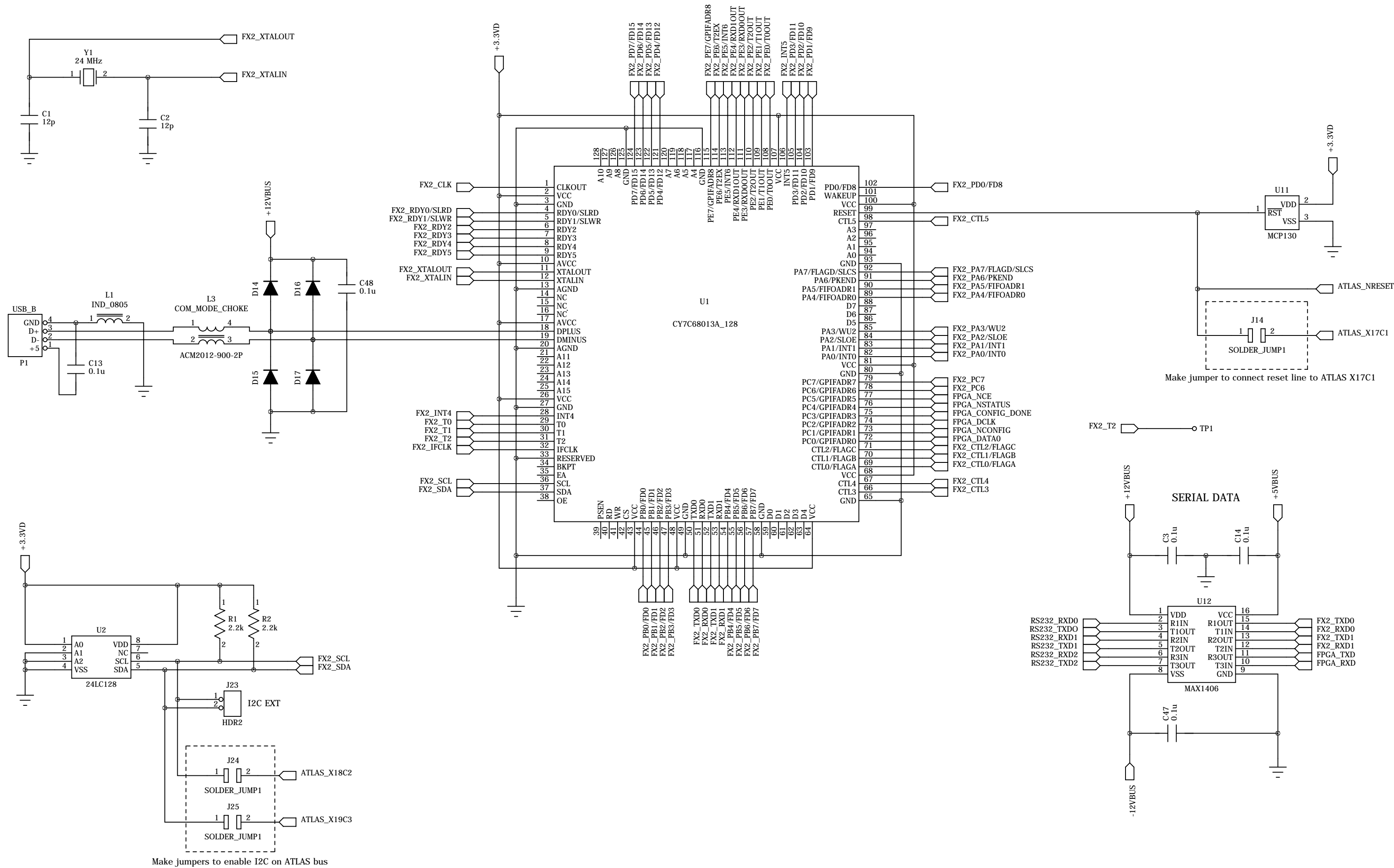
C

B

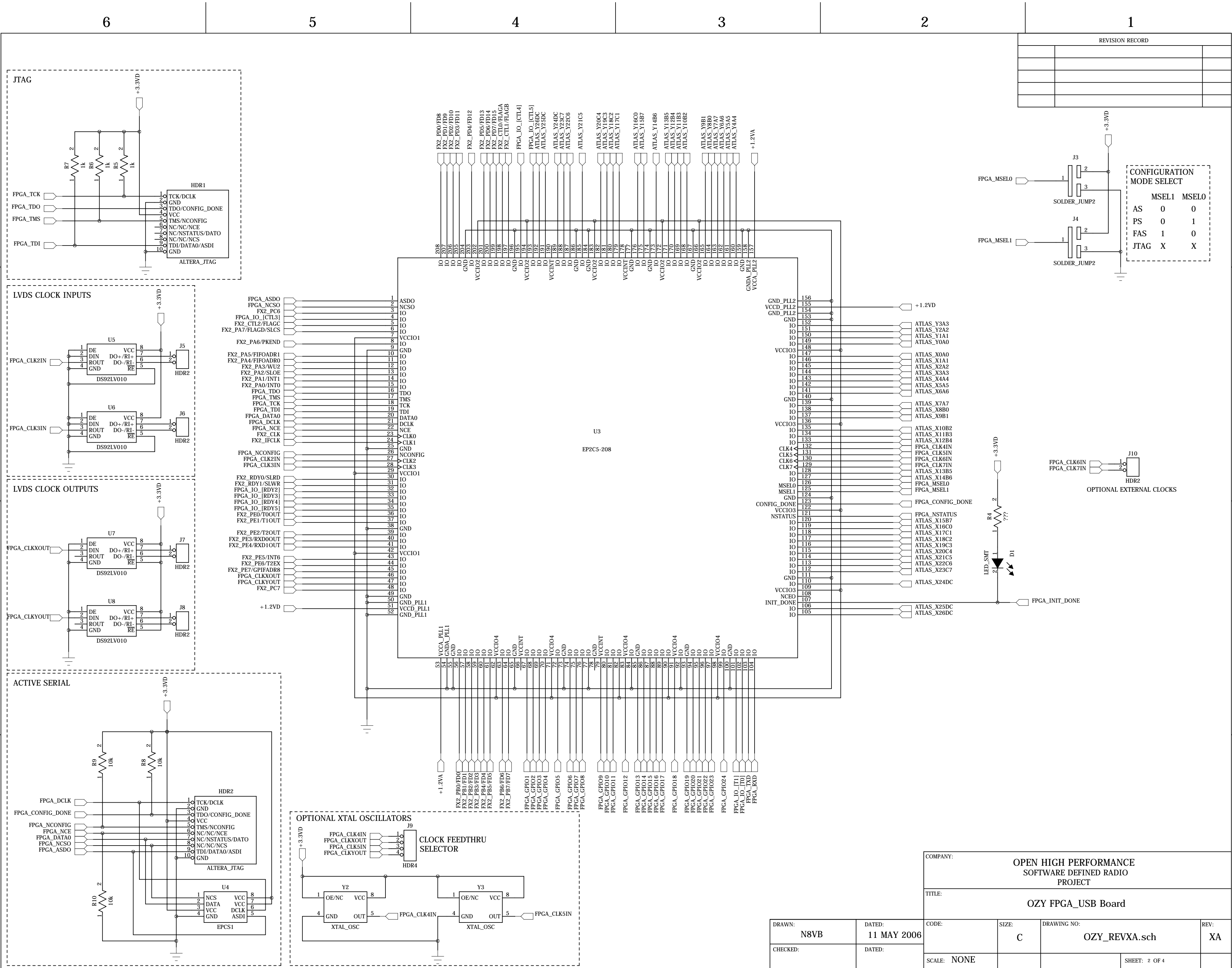
B

A

A



COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT		
TITLE:		OZY FPGA_USB Board		
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:
N8VB	11 MAY 2006		C	OZY_REVXA.sch
CHECKED:	DATED:	SCALE:	NONE	REV:
				XA
		SHEET: 1 OF 4		



REVISION RECORD		

CONFIGURATION MODE SELECT			
	MSEL1	MSEL0	
AS	0	0	
PS	0	1	
FAS	1	0	
JTAG	X	X	

DRAWN: N8VB		DATED: 11 MAY 2006		CODE:	SIZE: C	DRAWING NO: OZY_REVXA.sch	REV: XA
CHECKED:		DATED:		SCALE: NONE		SHEET: 2 OF 4	

