

DRAWN: N8VB	DATED: 17 MAY 2006	CODE:	SIZE: C	DRAWING NO. OZY_REVXA4.sch	REV: XA.4
CHECKED:	DATED:	SCALE: NONE		SHEET: 1 OF 4	



Make jumper to connect reset line to ATLAS X17C1

D

C

B

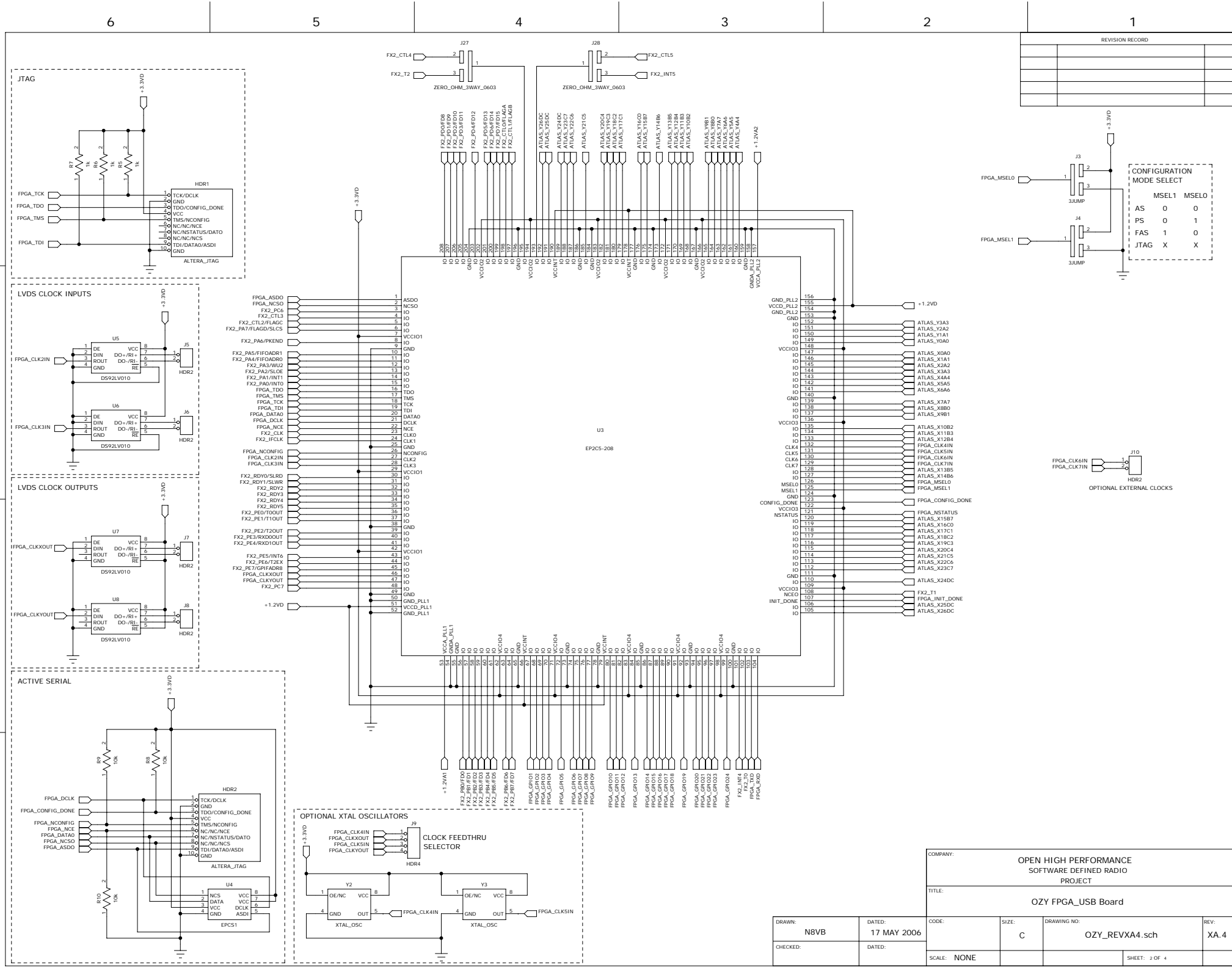
A

D

C

B

A



REVISION RECORD		

CONFIGURATION MODE SELECT		
MSEL1	MSEL0	
AS	0	0
PS	0	1
FAS	1	0
JTAG	X	X

FPGA_CLK6IN	FPGA_CLK7IN	J10

OPTIONAL EXTERNAL CLOCKS

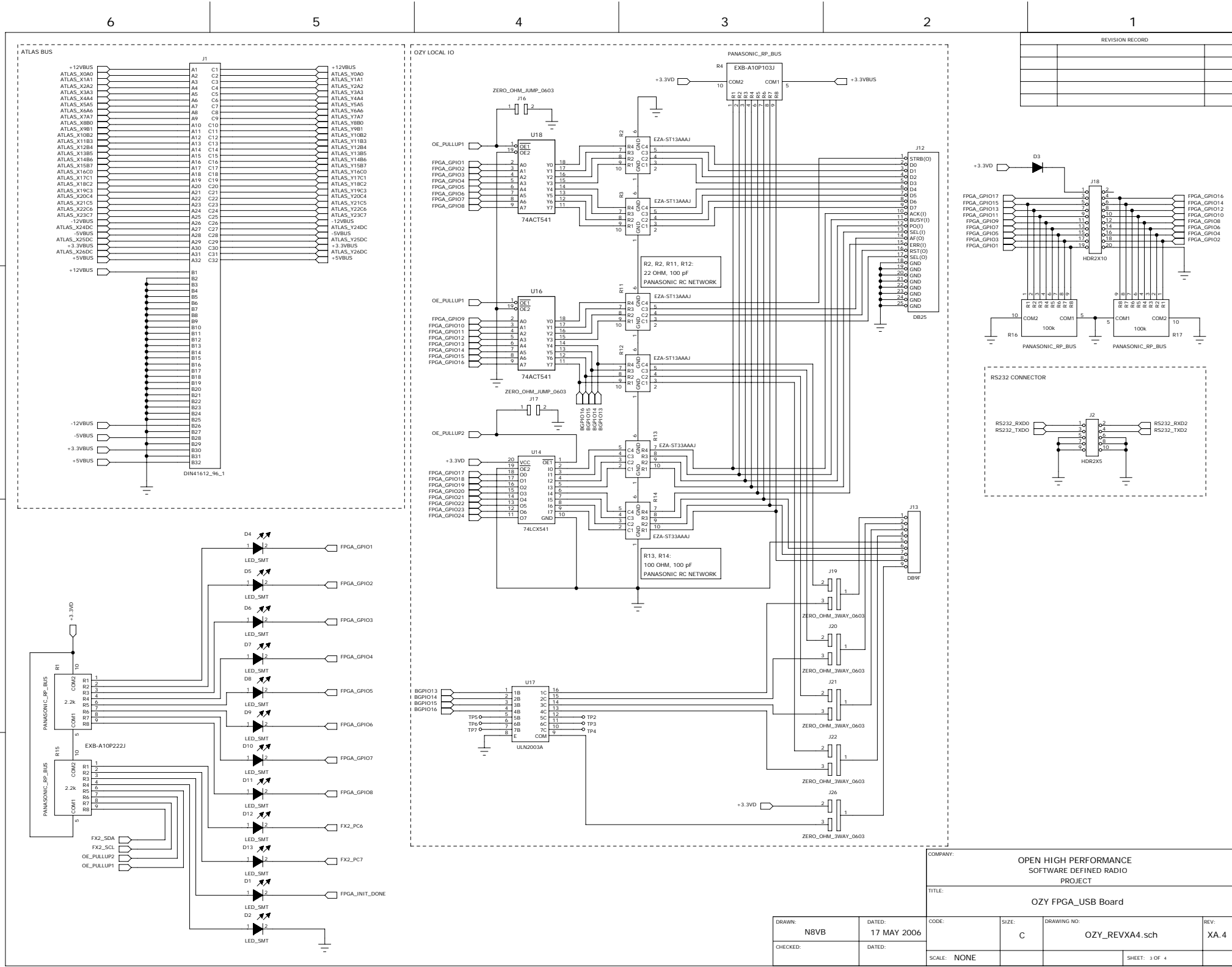
COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
DRAWN:	N8VB	DATED:	17 MAY 2006	CODE:	
CHECKED:		DATED:		SIZE:	C
SCALE:		NONE		DRAWING NO:	OZY_REVX4.sch
				REV:	XA.4
				SHEET:	2 OF 4

D

C

B

A



D

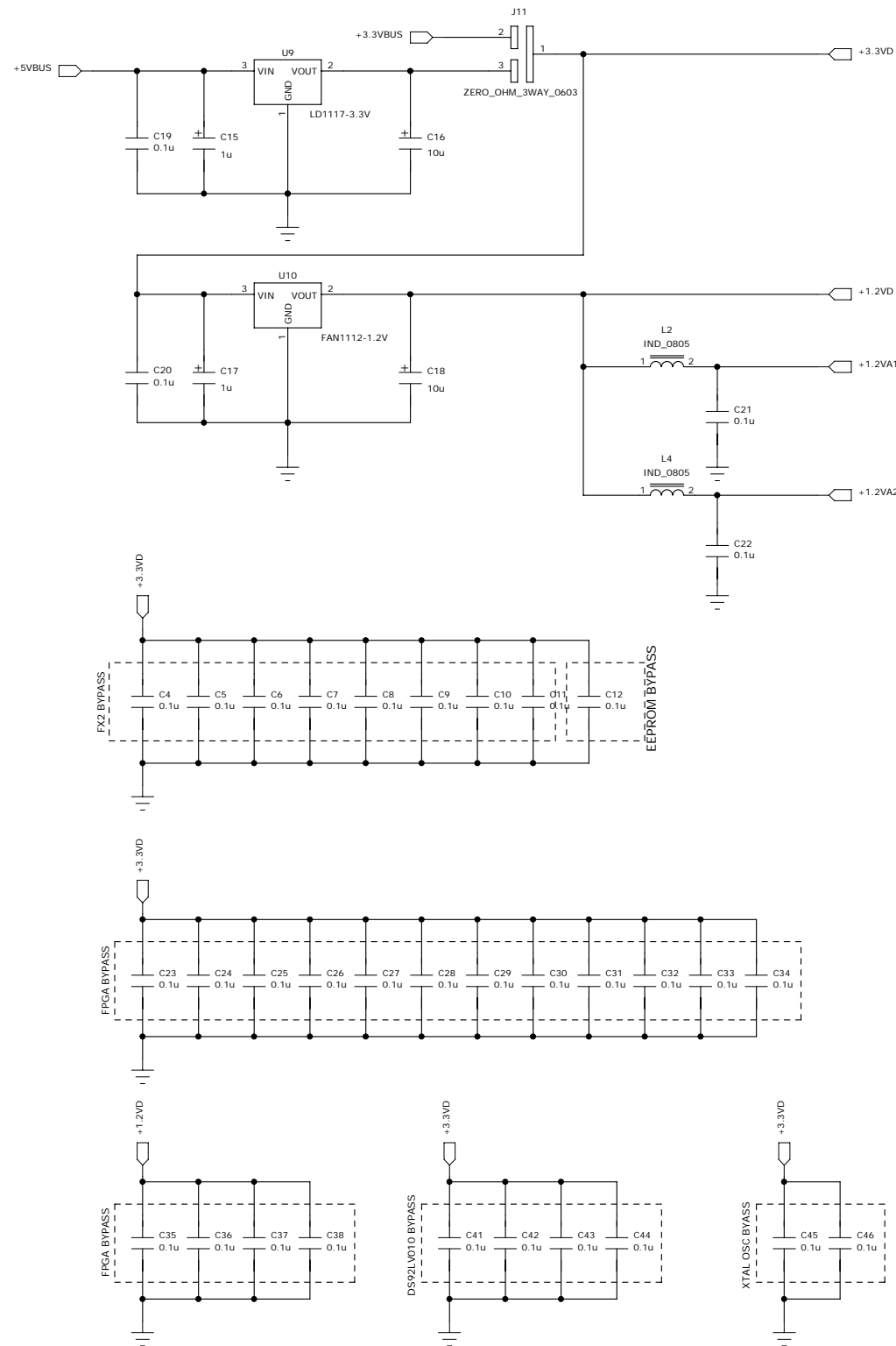
C

B

A

COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
DRAWN:	N8VB	DATED:	17 MAY 2006	CODE:	
CHECKED:		DATED:		SIZE:	C
		SCALE: NONE		DRAWING NO:	OZY_REVXA4.sch
				SHEET:	3 OF 4
				REV:	XA.4

POWER SUPPLY



FPGA_IO_TERMINATIONS

