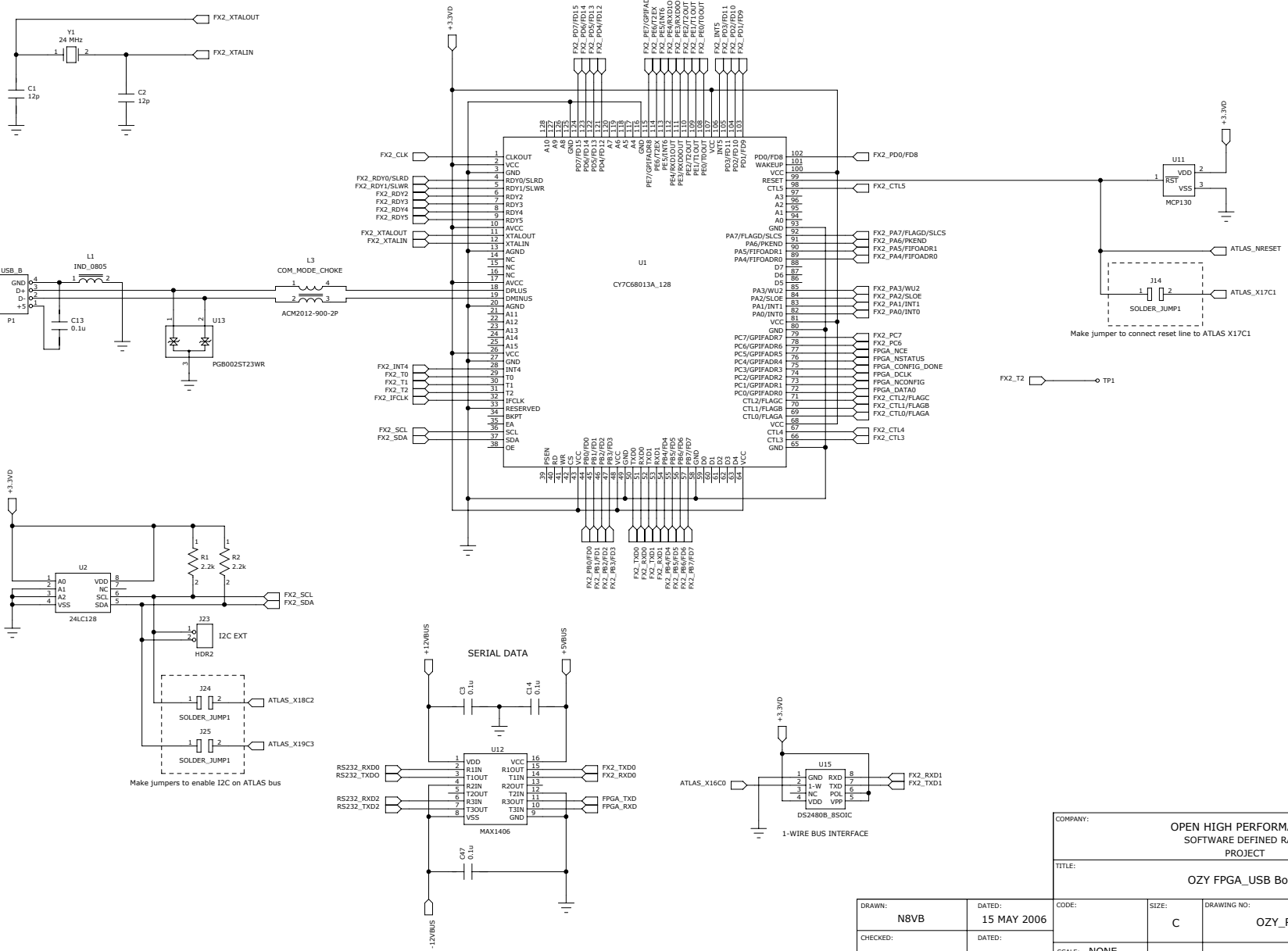


REVISION RECORD		



COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
DRAWN:	N8VB	DATED:	15 MAY 2006	CODE:	
CHECKED:		DATED:		SIZE:	C
SCALE:		NONE		DRAWING NO:	OZY_REVXA2.sch
				REV:	XA.2
				SHEET: 1 OF 4	

D

C

B

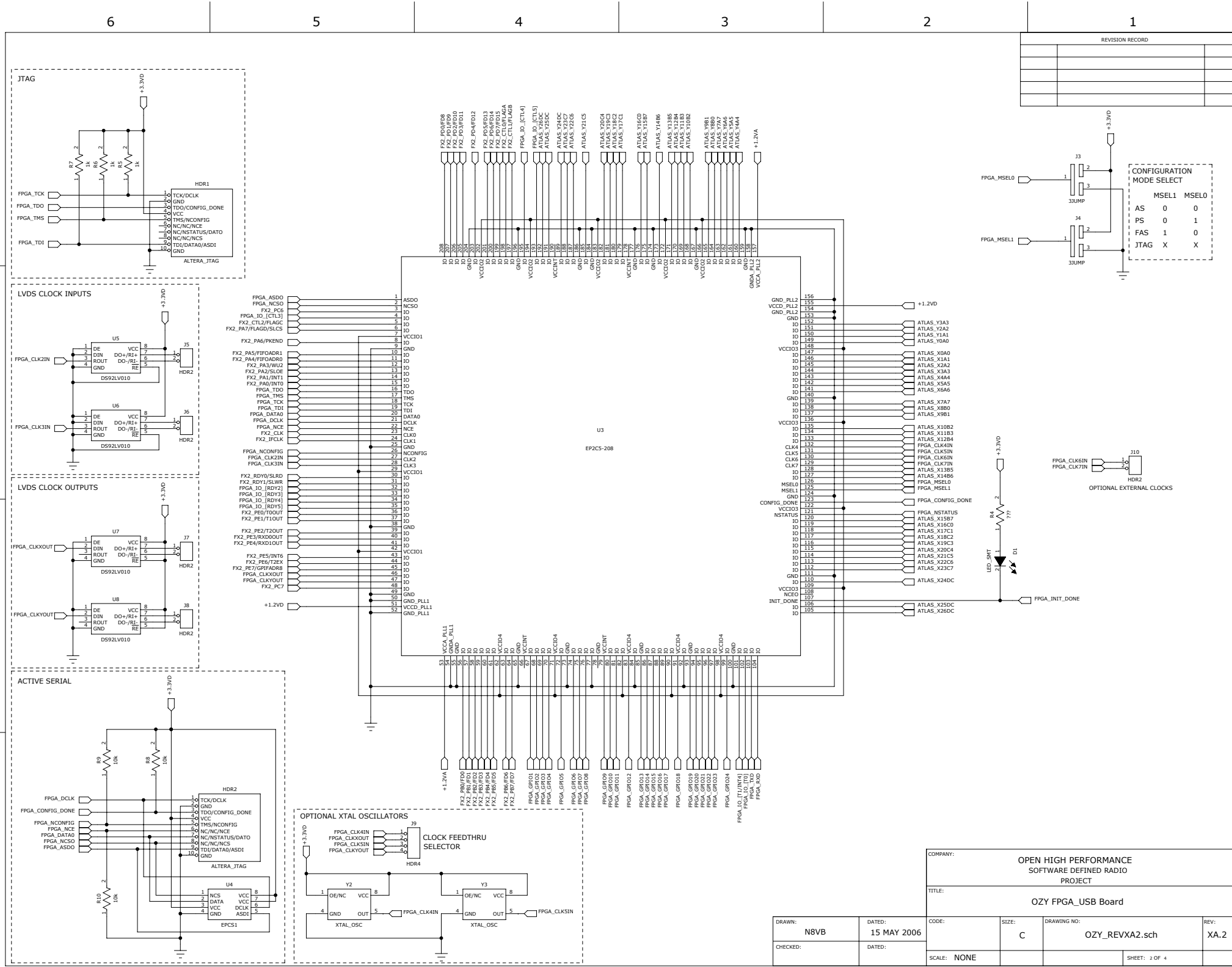
A

D

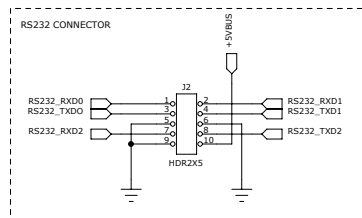
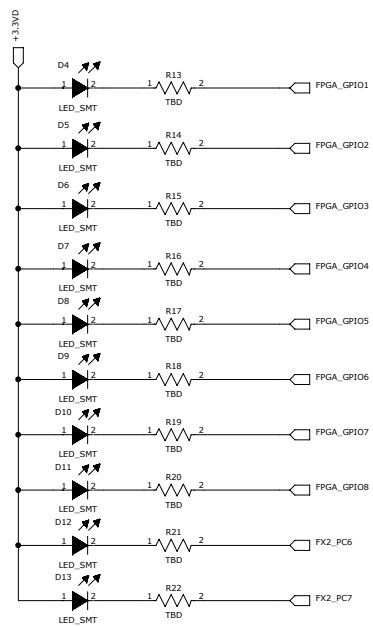
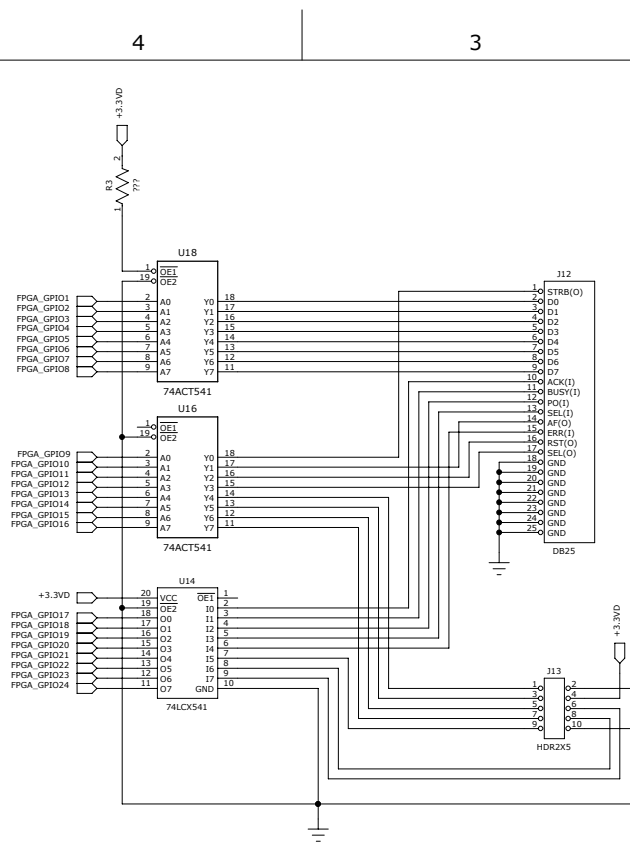
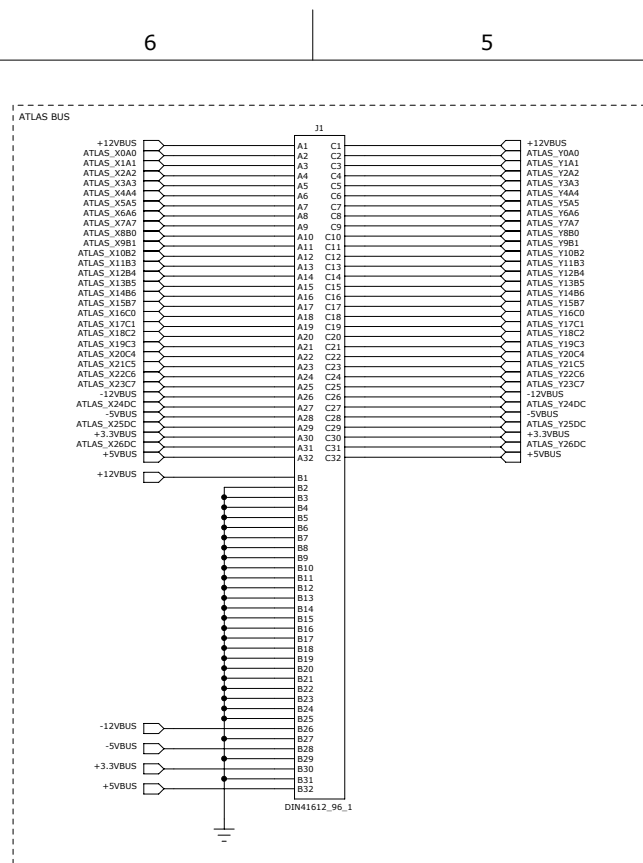
C

B

A



COMPANY:		OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
TITLE:		OZY FPGA_USB Board			
DRAWN:	DATED:	CODE:	SIZE:	DRAWING NO:	REV:
N8VB	15 MAY 2006		C	OZY_REVXA2.sch	XA.2
CHECKED:	DATED:	SCALE:	NONE	SHEET: 2 OF 4	



REVISION RECORD		

		COMPANY: OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT			
		TITLE: OZY FPGA_USB Board			
DRAWN: N8VB	DATED: 15 MAY 2006	CODE:	SIZE: C	DRAWING NO: OZY_REVXA2.sch	REV: XA.2
CHECKED:	DATED:	SCALE: NONE		SHEET: 3 OF 4	

6

5

4

3

2

1

REVISION RECORD

D

D

C

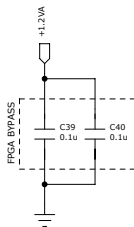
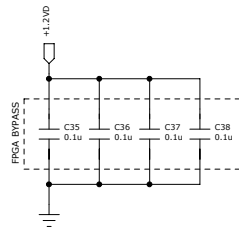
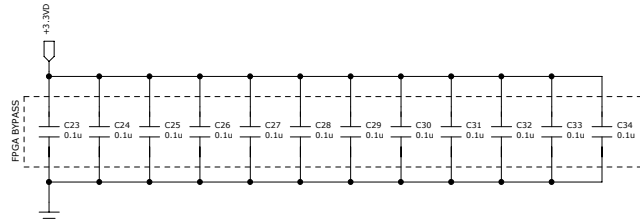
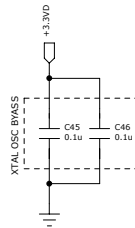
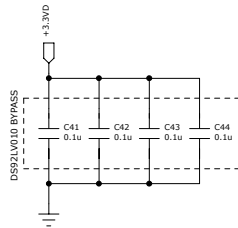
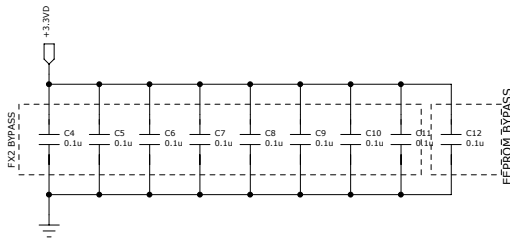
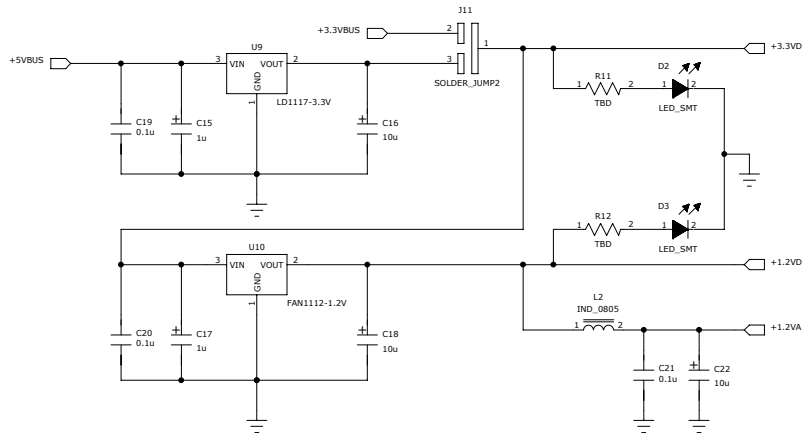
C

B

B

A

A



COMPANY: OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT					
TITLE: OZY FPGA_USB Board					
DRAWN: N8VB	DATED: 15 MAY 2006	CODE:	SIZE: C	DRAWING NO: OZY_REVXA2.sch	REV: XA.2
CHECKED:	DATED:	SCALE: NONE		SHEET: 4 OF 4	