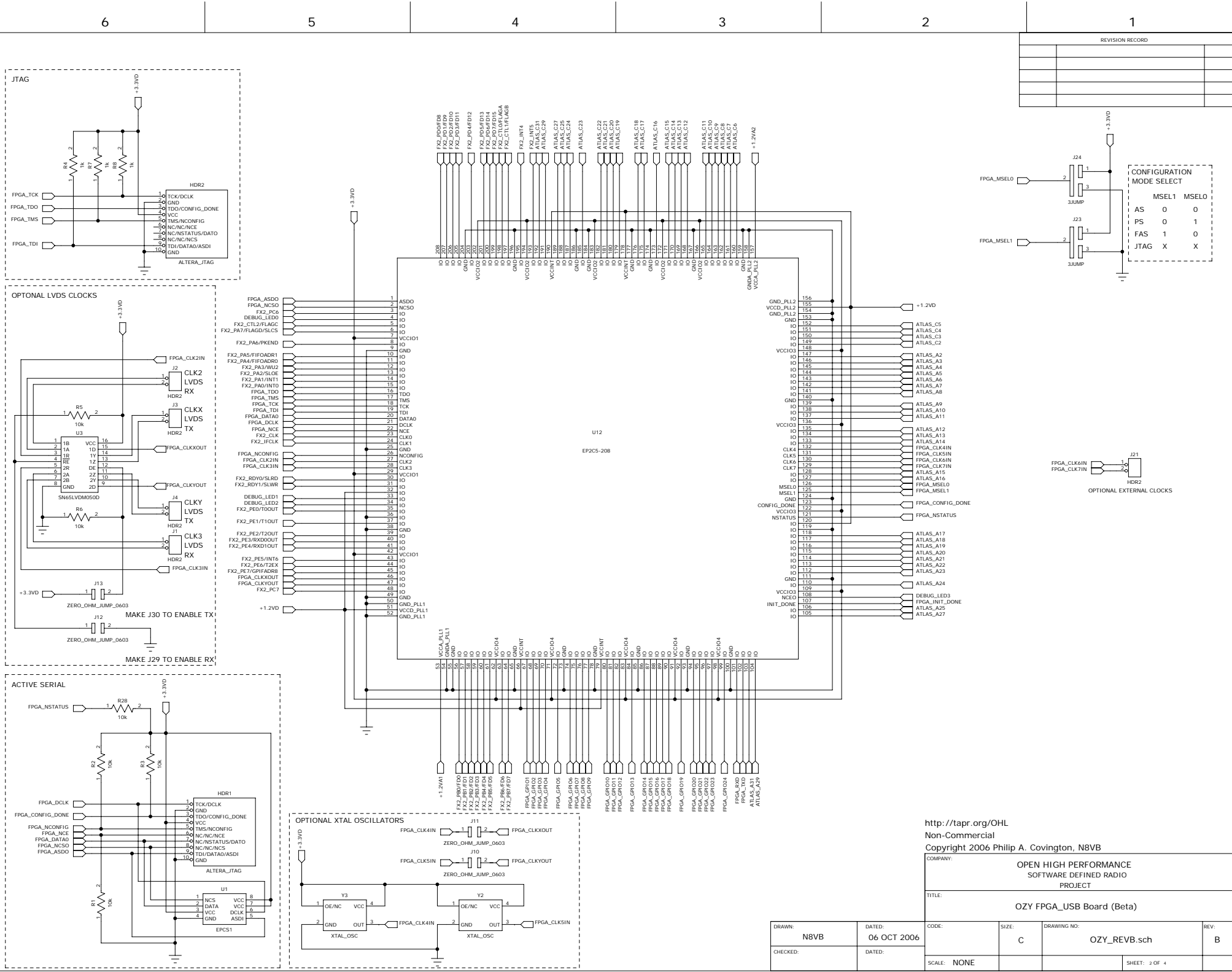


D

C

B

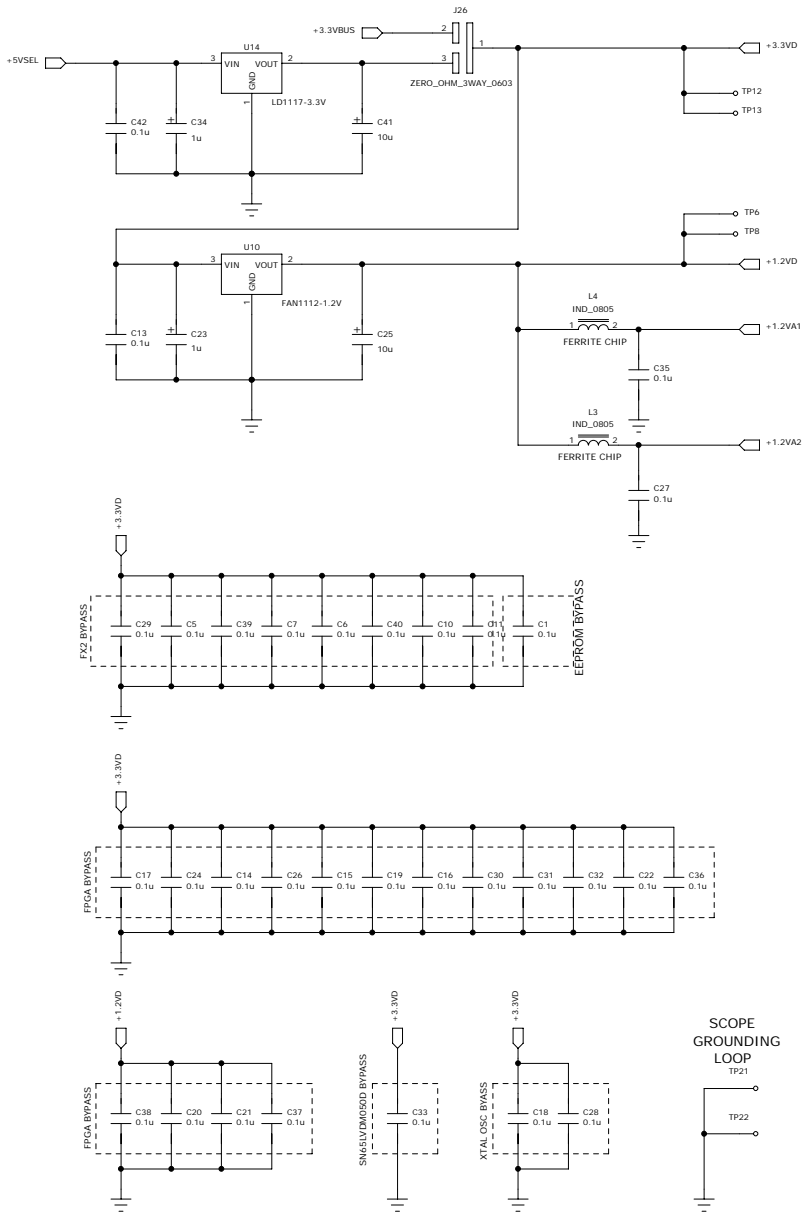
A



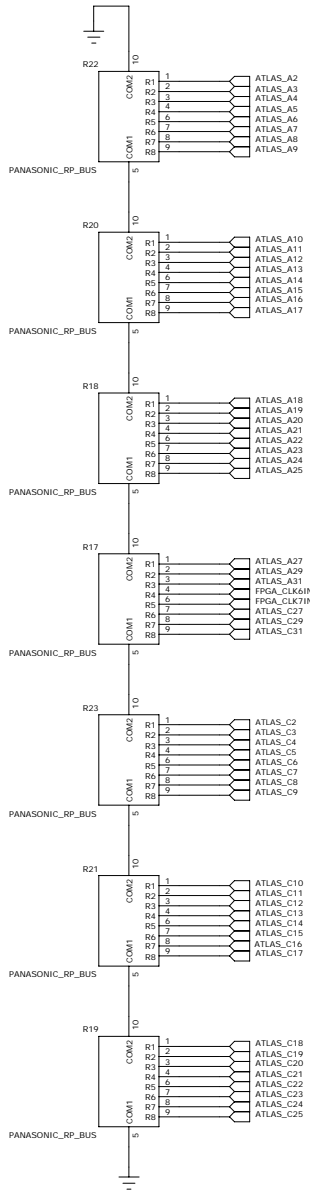
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TITLE: OZY FPGA_USB Board (Beta)					
DRAWN: N8VB	DATED: 06 OCT 2006	CODE:	SIZE: C	DRAWING NO: OZY_REV.B.sch	REV: B
CHECKED:	DATED:	SCALE: NONE		SHEET: 2 OF 4	

REVISION RECORD		

POWER SUPPLY



FPGA_IO_TERMINATIONS



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PROJECT

TITLE: OZY FPGA_USB Board (Beta)

DRAWN: N8VB	DATED: 06 OCT 2006	CODE:	SIZE: C	DRAWING NO: OZY_REV.B.sch	REV: B
CHECKED:	DATED:	SCALE: NONE		SHEET: 4 OF 4	