

6

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4

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2

1

REVISION RECORD

D

D

C

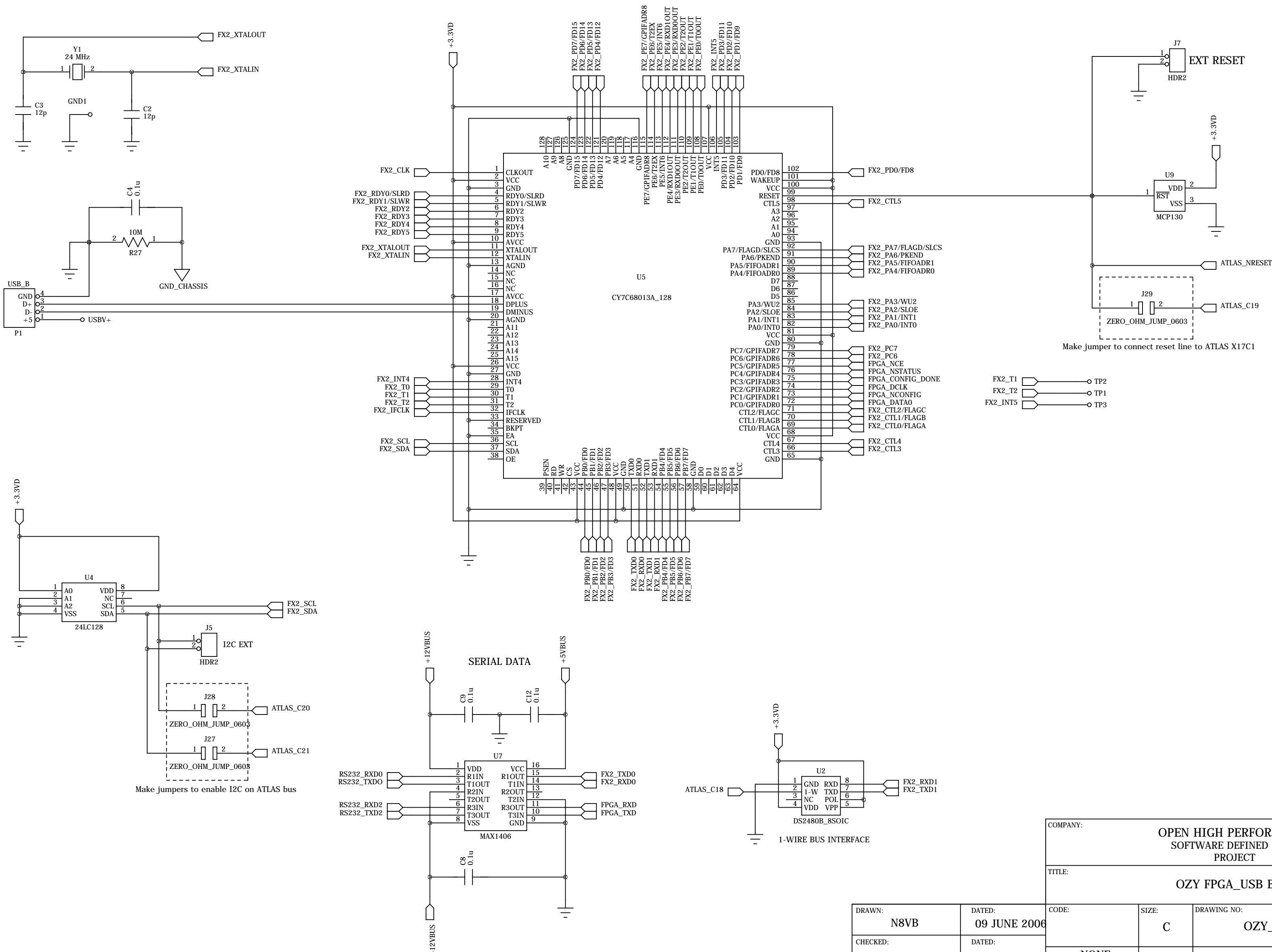
C

B

B

A

A



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D

D

C

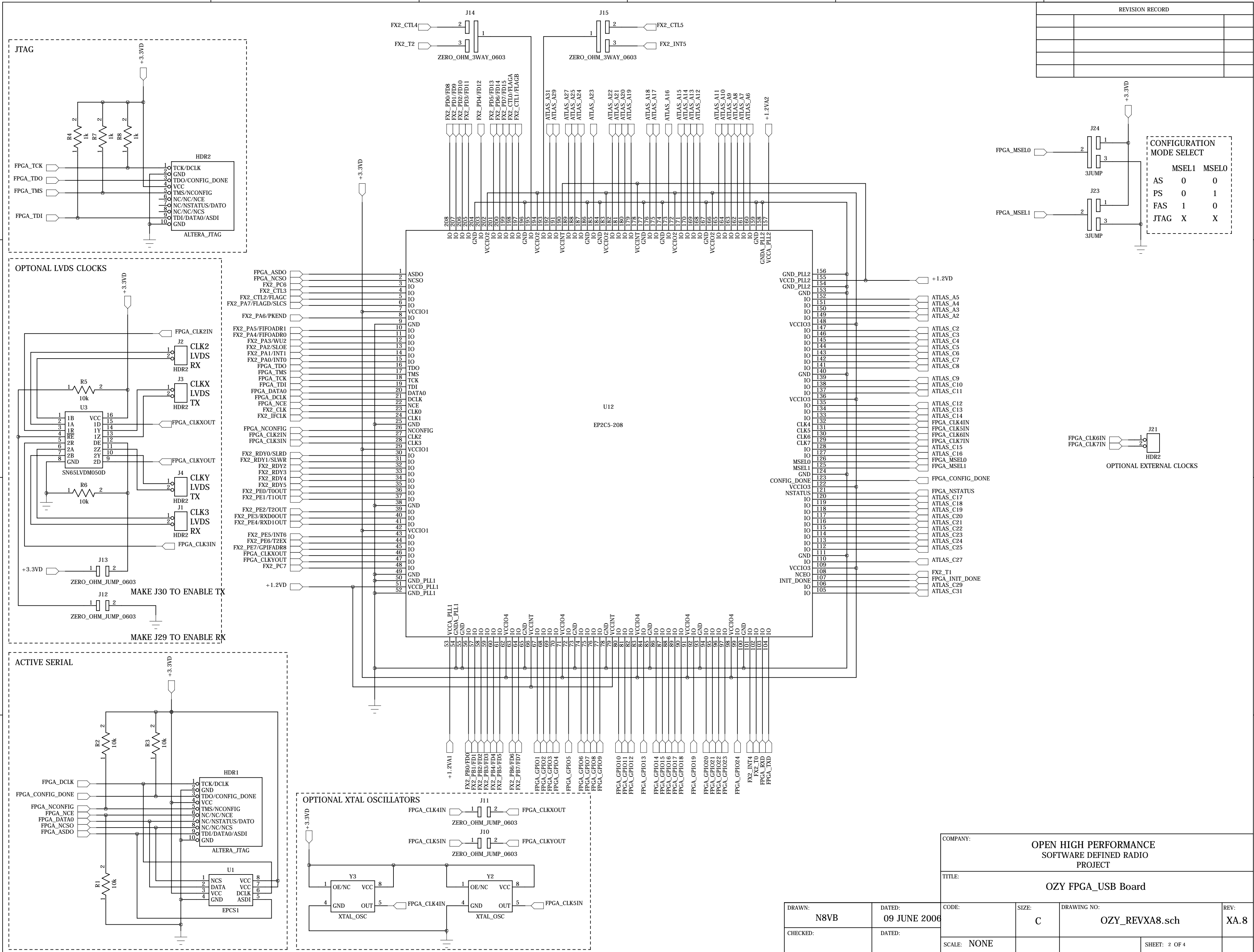
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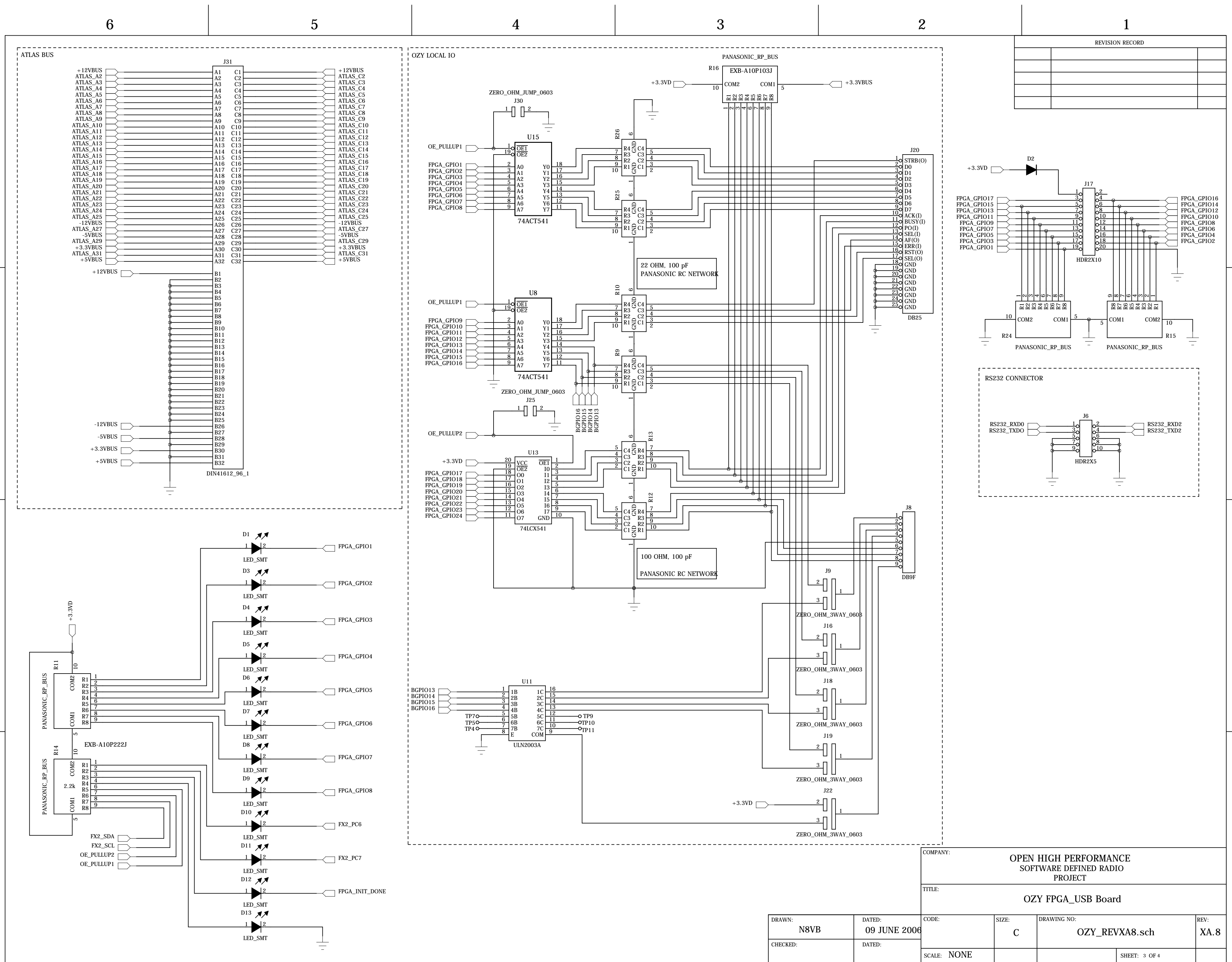
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B

A

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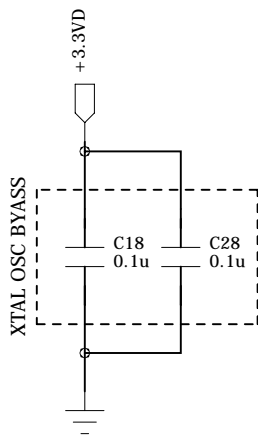
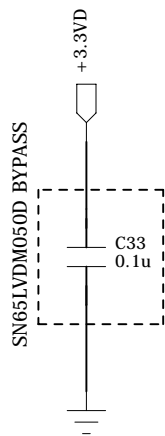
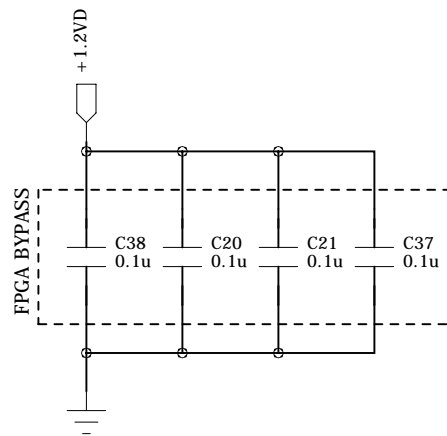
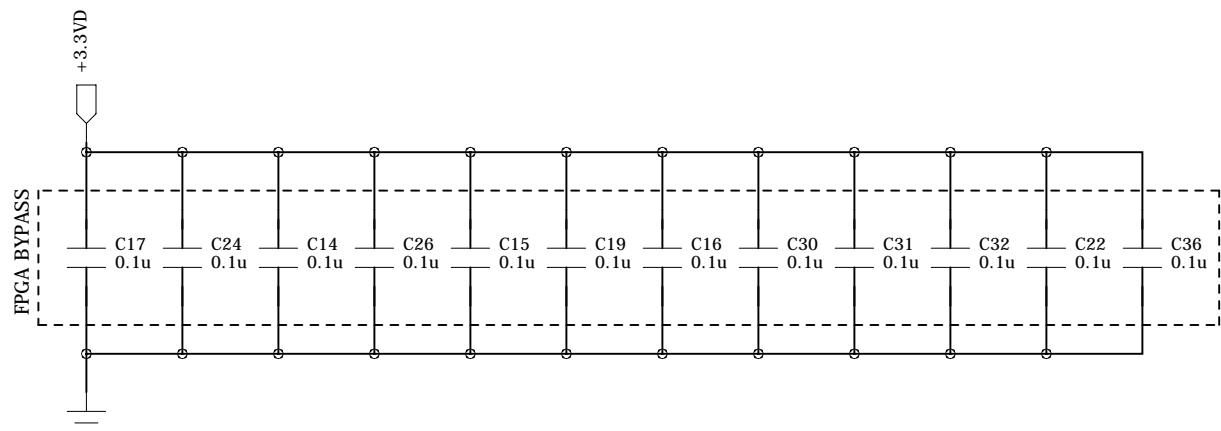
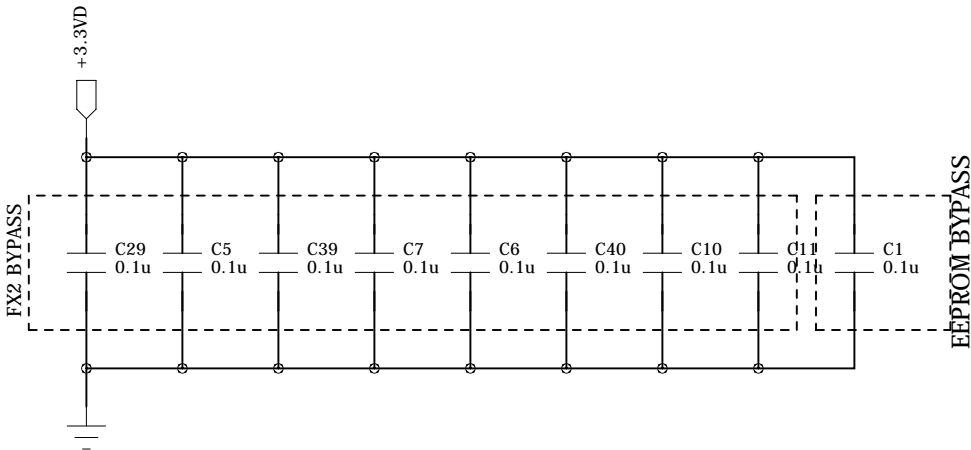
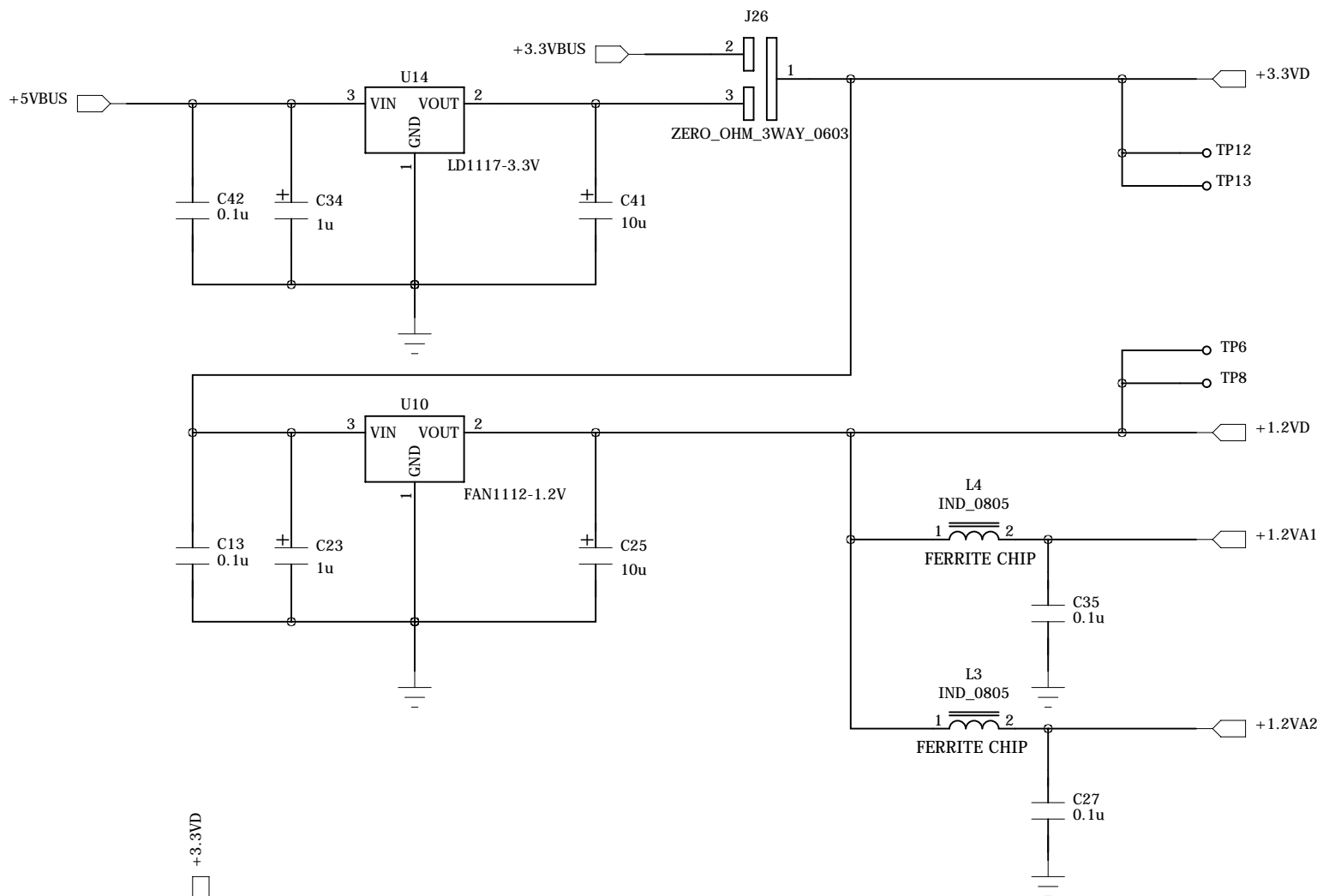
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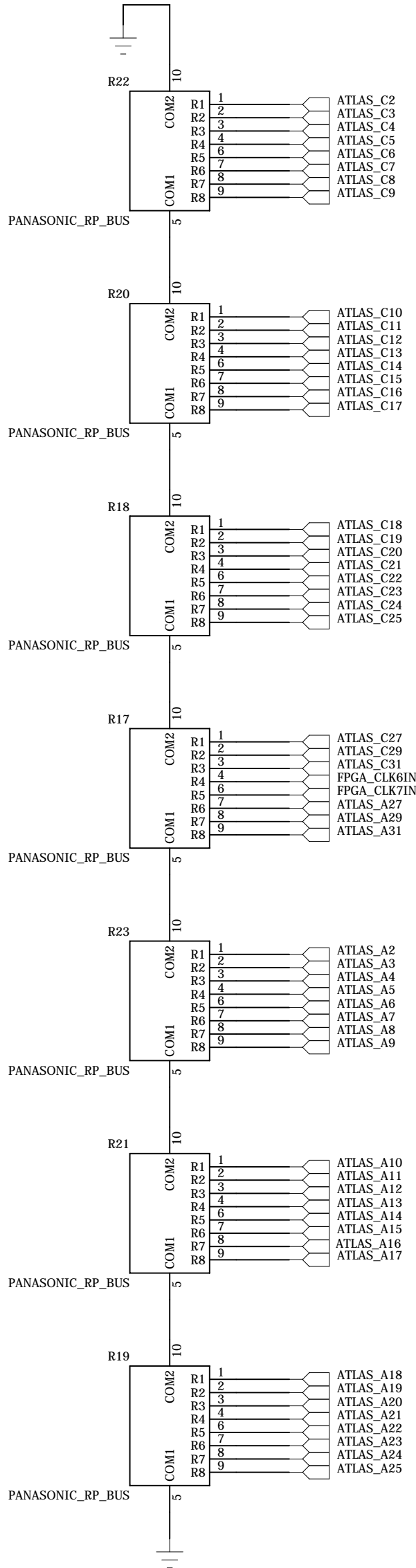
1

REVISION RECORD

POWER SUPPLY



FPGA_IO_TERMINATIONS



COMPANY: OPEN HIGH PERFORMANCE SOFTWARE DEFINED RADIO PROJECT				
TITLE: OZY FPGA_USB Board				
DRAWN: N8VB	DATED: 09 JUNE 2006	CODE:	SIZE: C	DRAWING NO: OZY_REVXA8.sch
CHECKED:	DATED:	SCALE: NONE		REV: XA.8
			SHEET: 4 OF 4	