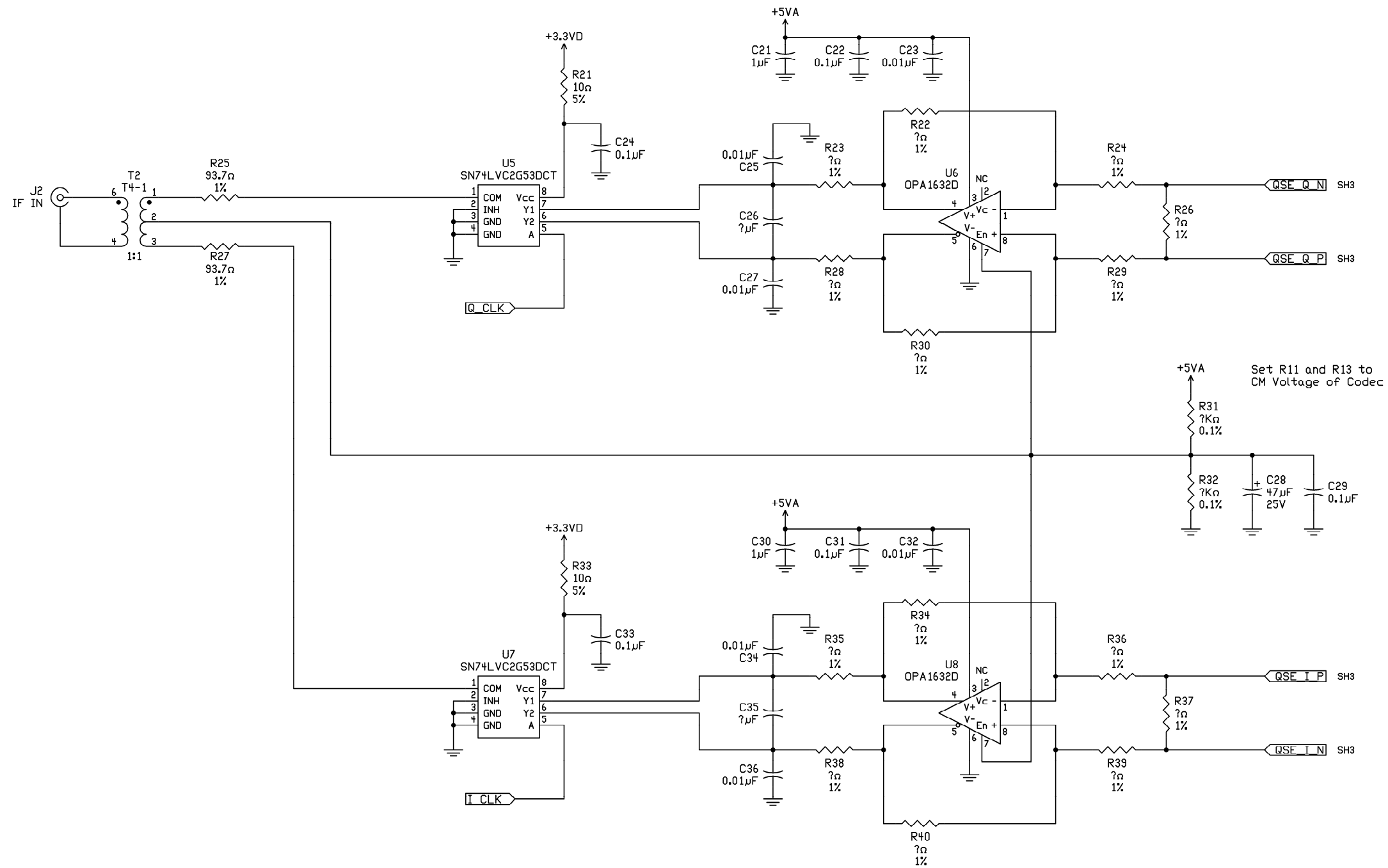


For Vdd = +5V, input voltage:
+1.5 to +4.0 VDC (delta 2.5 VDC)

For Vdd = +5V and 2K ohm load, drive voltage:
+1.9 to 3.1 VDC (delta 1.2 VDC)

Co:	TAPR http://www.tapr.org
Title:	HPSDR Project Oydessy
Board:	ISD
Author:	Steven Bible, N7HPR
Date:	October 27, 2006
Revision:	B
Size:	B
Sheet	1 of 4

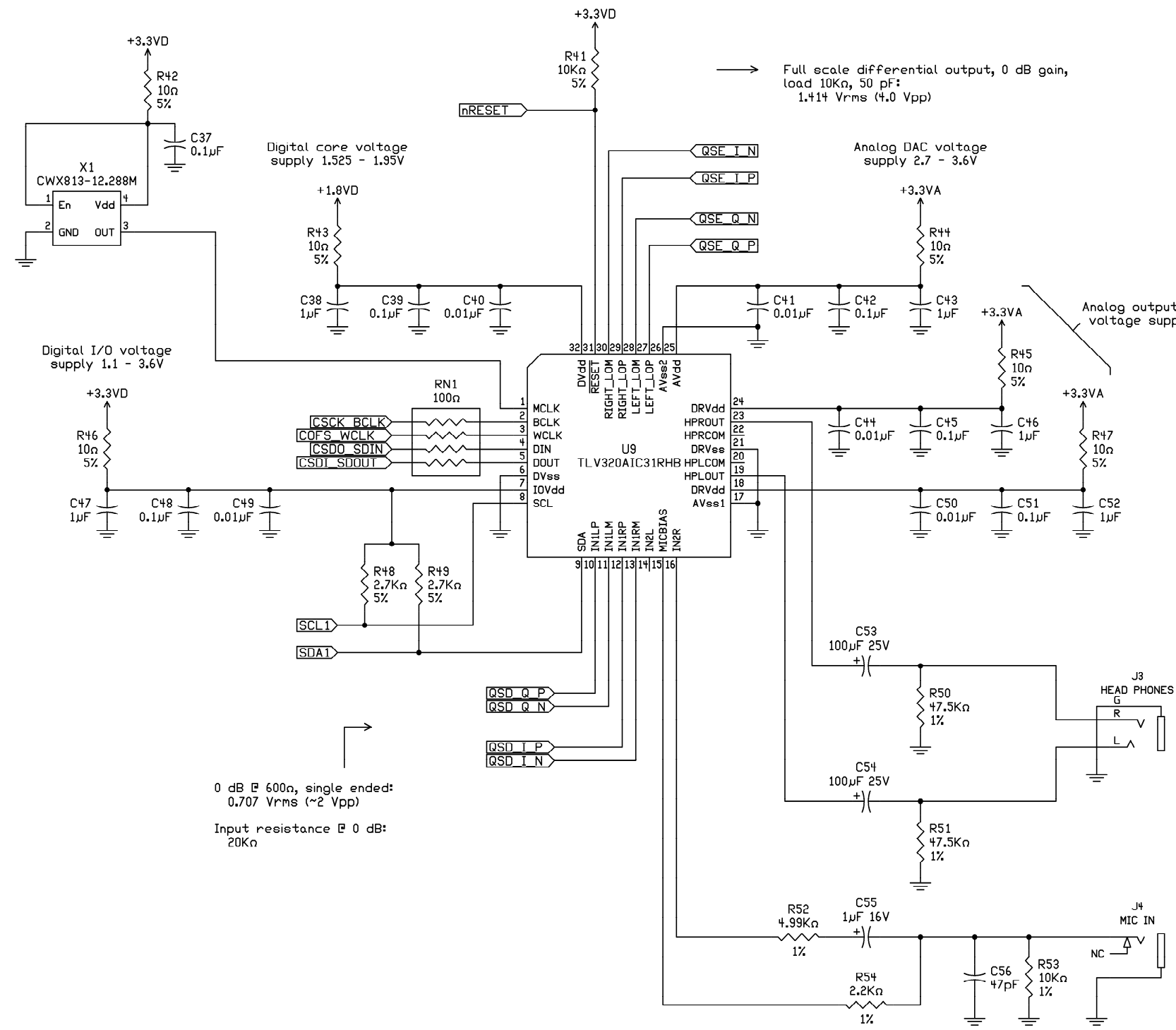


Set R11 and R13 to CM Voltage of Codec

←
For Vdd = +5V and 2K ohm load, drive voltage:
+1.9 to 3.1 VDC (delta 1.2 VDC)

←
For Vdd = +5V, input voltage:
+1.5 to +4.0 VDC (delta 2.5 VDC)

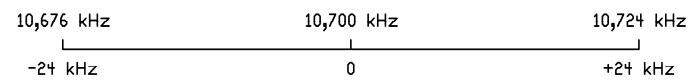
Co:	TAPR http://www.tapr.org
Title:	HPSDR Project Oydessy
Board:	QSE
Author:	Steven Bible, N7HPR
Date:	October 27, 2006
Revision:	B
Size:	B
Sheet	2 of 4



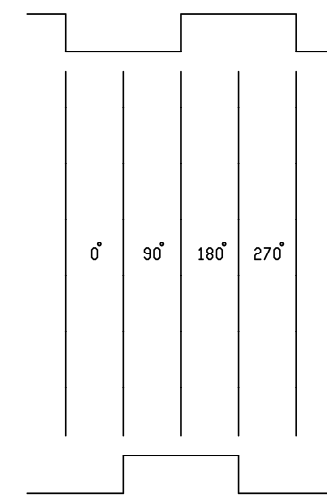
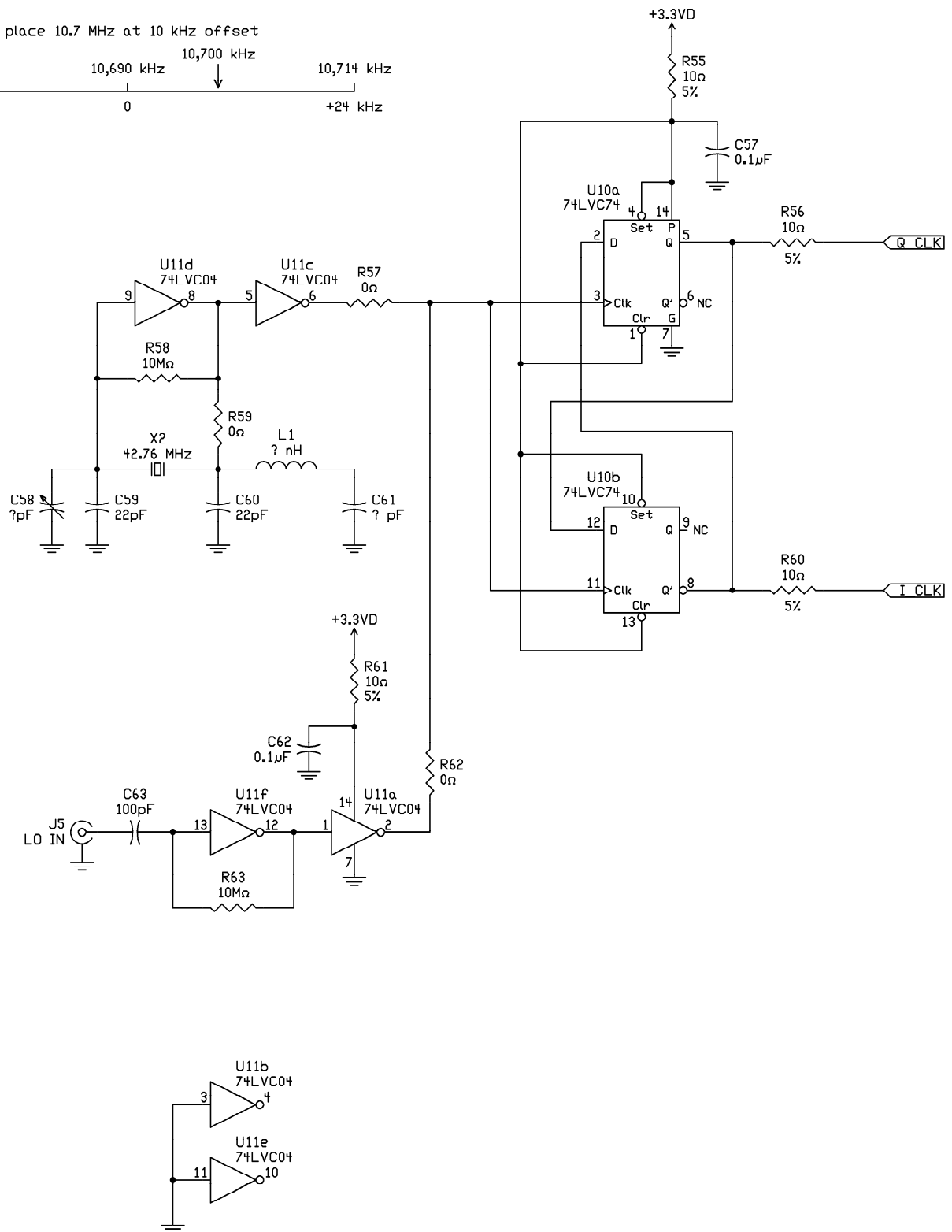
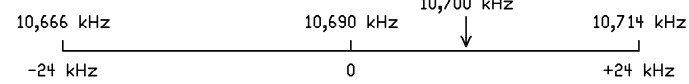
Codec supply current:
 Stereo line playback:
 AVdd + DRVdd = 3.0 mA
 DVdd = 2.0 mA
 Stereo record:
 AVdd + DRVdd = 4.3 mA
 DVdd = 1.3 mA

Co:	TAPR http://www.tapr.org
Title:	HPSDR Project Odyessy
Board:	TLV320AIC31 Codec
Author:	Steven Bible, N7HPR
Date:	October 27, 2006
Revision:	B
Size:	B
Sheet:	3 of 4

42.80 MHz will place 10.7 MHz at DC



42.76 MHz will place 10.7 MHz at 10 kHz offset



Co:	TAPR http://www.tapr.org
Title:	HPSDR Project Oydessy
Board:	I/Q Clock
Author:	Steven Bible, N7HPR
Date:	October 27, 2006
Revision:	B
Size:	B
Sheet:	4 of 4